

#### Building NanoVDBs on the GPU Greg Klar, Ken Museth



#### **Use case: Particle Rasterization**







Top: NanoVDB rasterizer; Bottom: OpenVDB rasterizer





by Andre Pradhana



## Building a NanoVDB from points



Snapshot of the source under QR code

Supports building of regular grids, point grids, index grids

- Related use cases:
  - Point rasterization
  - Point-to-grid transfers



#### NanoVDB Principles

- Pointerless: uses relative offsets in memory
- Very versatile across architectures
- Not well suited for incremental building
- Consequence: Need to know the memory footprint of the grid first!



#### NanoVDB Building Steps

- Allocate memory
- Build tree and populate values







\*nodes not to scale Default configuration: 8^3 voxels per leaf, 16^3 leaves per lower node, 32^3 lower nodes per upper node.

#### NanoVDB Nodes



\*nodes not to scale

Voxels, your data lives here!

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#### How Much Memory to Allocate?

NanoVDB footprint



Same in this use case



#### NanoVDB Building Steps Corrected

- Count nodes
- Allocate memory
- Build tree and populate values





# COUNTING NODES

#### - INTERMISSION -

Binning points on the GPU

- Sort + RLE + PrefixSum = binning
- RadixSort:
  - Sort based on a key the defines the binning  $\rightarrow$  elements in the same bin will be consecutive
- Run Length Encode
  - $\blacktriangleright$   $\rightarrow$  number of elements per bin and the number of bins
- Exclusive Sum (aka *PrefixSum* aka *Scan*)
  - $\rightarrow$  indices to the start of each bin in the sorted array
- All these are available in CUB!



### **COUNTING ROOT TILES**

- Binning the particle IDs by their Root Key
  - **Root keys are available from** nanovdb::RootData::CoordToKey
- Steps:
  - Generate (root key, point ID) pairs for each point based on their index-space location
  - Radix Sort pairs base on root key
  - Run Length Encode, outputs:









#### **INDEXING WITHIN A TILE**

New 64 bits *voxel key* for each point: 

> 28b 12b 9b 15b

- 9 bits for voxel offset
- 12 bits for *leaf offset*
- 15 bits for *lower offset*
- 28 bits for *tile ID* Not the same as tile key! This is the running index from 0..tile\_count-1



### **COUNTING ACTIVE VOXELS**

Plan: bin points to voxels  $\rightarrow$  number of unique bins == number of active voxels 



Exclusive Sum: offset to look up points based on voxel 



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### COUNTING LEAF NODES



Voxel keys are already sorted in



- Recall voxel keys are tile ID, lower offset, leaf offset, voxel offset
- Shift them right by 9 bits  $\rightarrow$  leaf keys





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## **COUNTING LOWER NODES**



Leaf keys are already sorted in 

d\_leaf\_keys

- Recall leaf keys are *tile ID*, *lower offset*, *bef offset*
- Shift them right by 12 bits  $\rightarrow$  lower node keys

28b 15b 12b

We don't need binning at this point, just the number and values of each lower node! 



d\_leaf\_keys

with a *Right Shift 12 Bits Iterator*, outputs:



lower node count



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#### Done counting the nodes!







## ALLOCATING BUFFER

#### **READY TO ALLOCATE MEMORY**

- Now that we have the number of nodes, we can allocate the buffer for the grid
- We are including the seed points as well in the blind data
- At this point we know the place in memory of all the nodes by ordinal indexing, eg. the nth lower node, but not by spatial coordinates







- NanoUpper<BuildT>& getUpper(int i) const {return \*(PtrAdd<NanoUpper<BuildT>>(d\_bufferPtr, upper)+i);}
- NanoLower<BuildT>& getLower(int i) const {return \*(PtrAdd<NanoLower<BuildT>>(d\_bufferPtr, lower)+i);}
- NanoLeaf<BuildT>& getLeaf(int i) const {return \*(PtrAdd<NanoLeaf<BuildT>>(d\_bufferPtr, leaf)+i);}
  - E.g. access to getLower(i) is valid, if 0<= i < lower node count!
- But we don't know their *spatial* positions!
  - E.g. given *ijk* coordinates, we don't know how to get to that leaf, even though it is allocated.

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# BUILDING THE TREE

### **BUILDING THE TREE**



### GRID, TREE, AND ROOT

CudaPointsToGrid::processGridTreeRoot

- Single-thread kernel
- Straightforward housekeeping



#### **UPPER NODES**

CudaPointsToGrid::processUpperNodes

- Running on # of upper nodes threads:
  - tid is upper node id. Get the nodes with getUpper
  - Ijk cords of the upper node: NanoRoot<uint32\_t>::KeyToCoord( d\_tile\_keys )
  - Records the upper node to the root tile
- Running on (# of upper nodes \* 2^15) threads:
  - Zeroing the tables of every upper nodes

[tid]);



#### LOWER NODES

CudaPointsToGrid::processLowerNodes

- Very similar as before, but
  - const uint64\_t lowerKey = d\_lower\_keys [tid];
  - auto &upper = d\_data->getUpper(lowerKey >> 15);
  - const uint32\_t upperOffset = lowerKey & 32767u;



- Needs to use atomic operations to set child mask in parent!
- New kernel launch for resetting the table



#### LEAF NODES

CudaPointsToGrid::processLeafNodes 

- For each leaf node
  - d\_leaf\_keys leafKey = [tid];
  - tile\_id = leafKey >> 27;
  - auto &upper = d\_data->getUpper(tile\_id);
  - const uint32\_t lowerOffset = leafKey & 4095u
  - upperOffset = (leafKey >> 12) & 32767u;
  - Record offset and point count in leaf, if building a point grid
- New kernel launch each active voxel: Record either point ID, or just 1 as a placeholder value

	4 = 1	4
	15D	

28b









≥ NVIDIA.

#### POINTS Only for point grids!

CudaPointsToGrid<Points>::processPoints

- Copy point IDs or values, based on grid type, into blind data
- Uses d\_indx for voxel to point ID lookup



#### COMPUTING THE BOUNDING BOXES

- Lower to upper nodes
- Upper to root nodes
- World space bounding box on grid

- All on the GPU
- Uses expandAtomic





## ENJOY YOUR BRAND NEW NANOVDB!

## THANK YOU!





