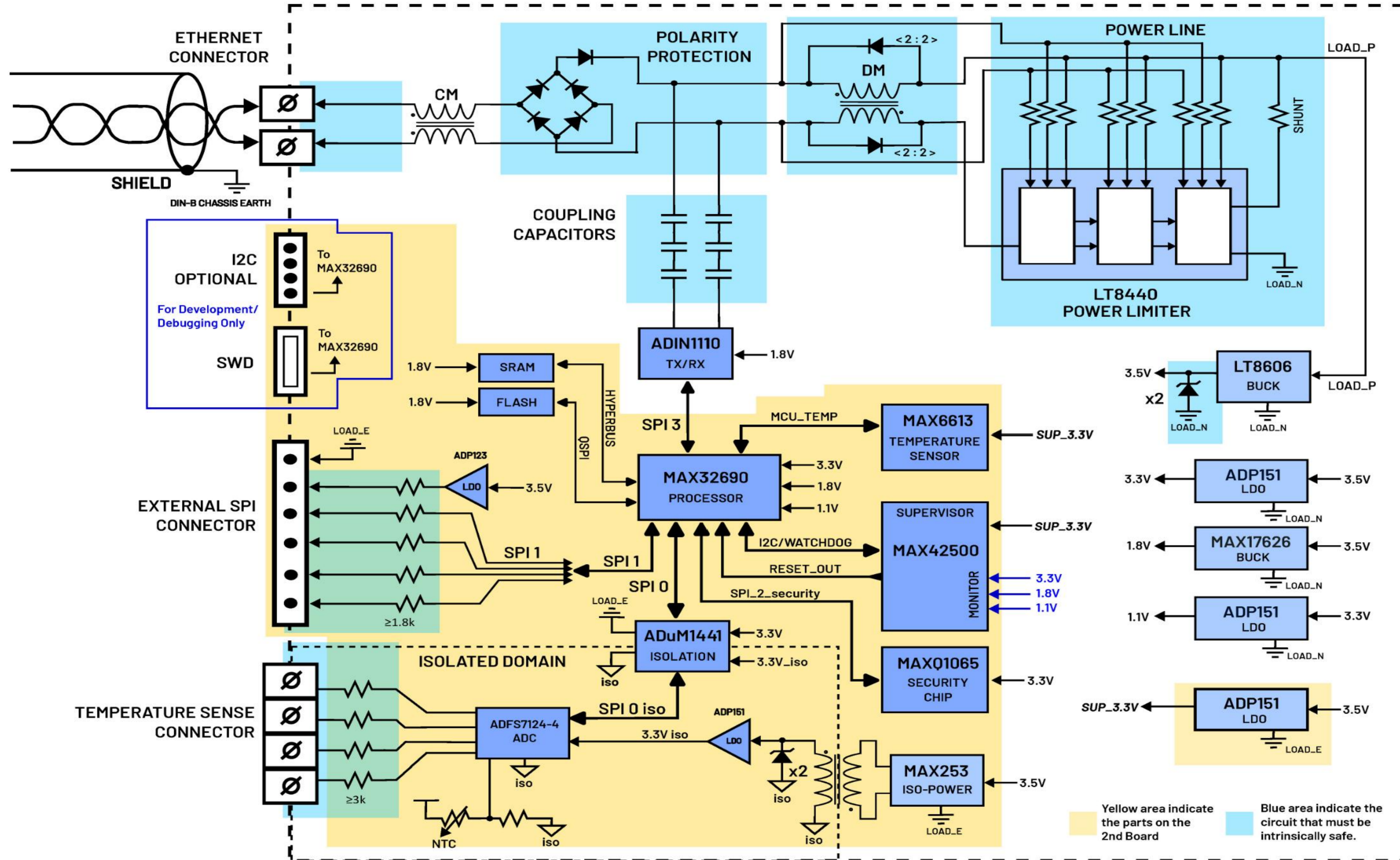


AD-ETHERNETAPLDEVICE-SL - BLOCK DIAGRAM

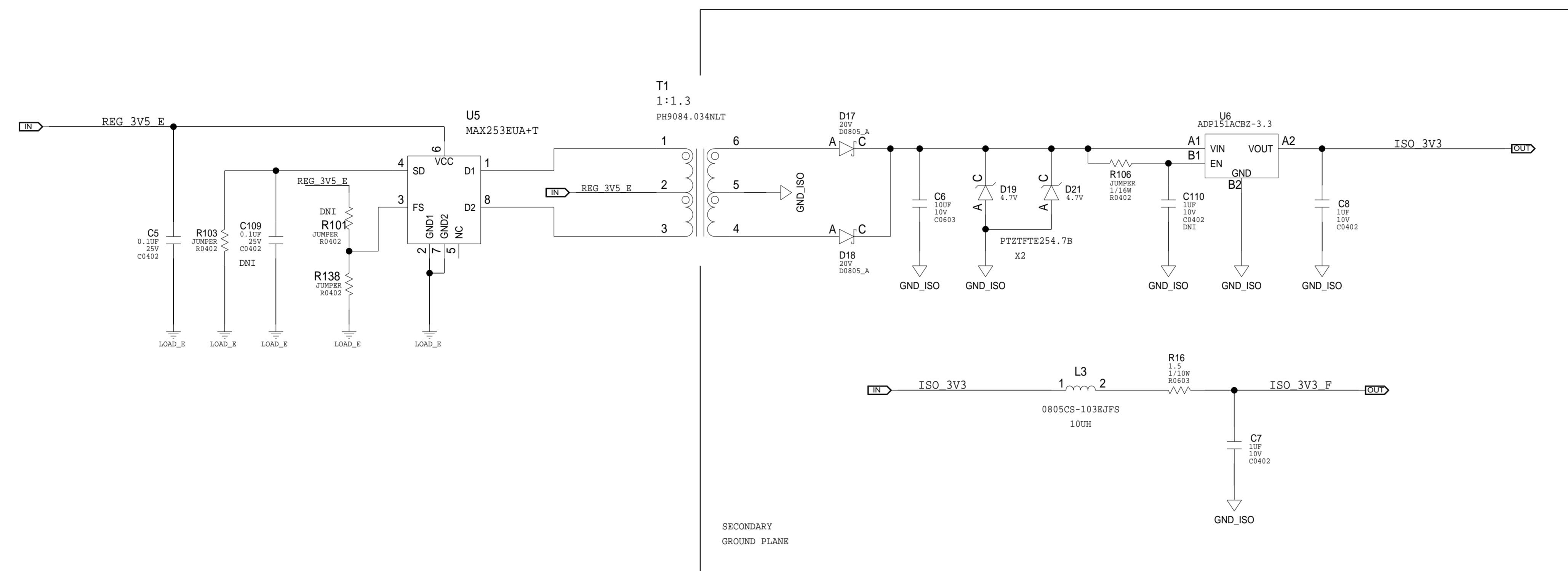
| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |



| | | | |
|------------------------------|---------------------------------|--------------|---------------|
| | SCHEMATIC | | |
| | HW TYPE : Customer Evaluation Z | | |
| | Product(s) : MAX32690 | | |
| | : ADFS7124-4 | | |
| DESIGN VIEW <DESIGN_VIEW> | DRAWING NO. 02_084576 | REV B | |
| PTD ENGINEER D. SUDOY | SIZE D | SCALE 1:1 | SHEET 2 OF 10 |

| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

POWER RAILS PATH ISOLATED POWER - MAX253



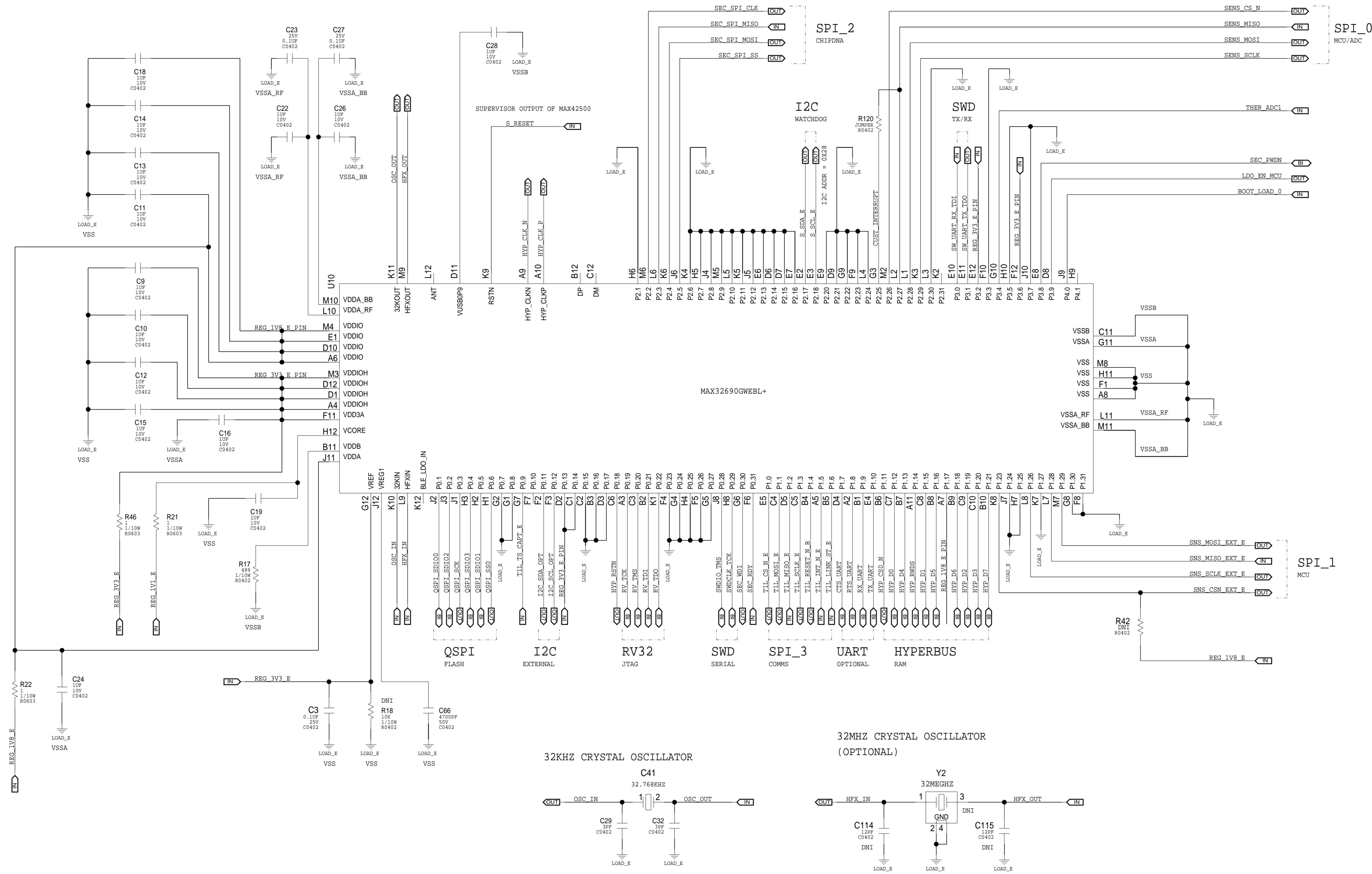
SECONDARY
GROUND PLANE

LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

| | | | |
|------------------------------|--|--------------|---------------|
| | SCHEMATIC | | |
| | HW TYPE : Customer Evaluation Z Product(s): MAX32690 : ADFS124-4 | | |
| DESIGN VIEW <DESIGN_VIEW> | DRAWING NO. 02_084576 | REV B | |
| PTD ENGINEER D. SUDOY | SIZE D | SCALE 1:1 | SHEET 3 OF 10 |

NETWORK SIGNAL PATH - MCU

| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

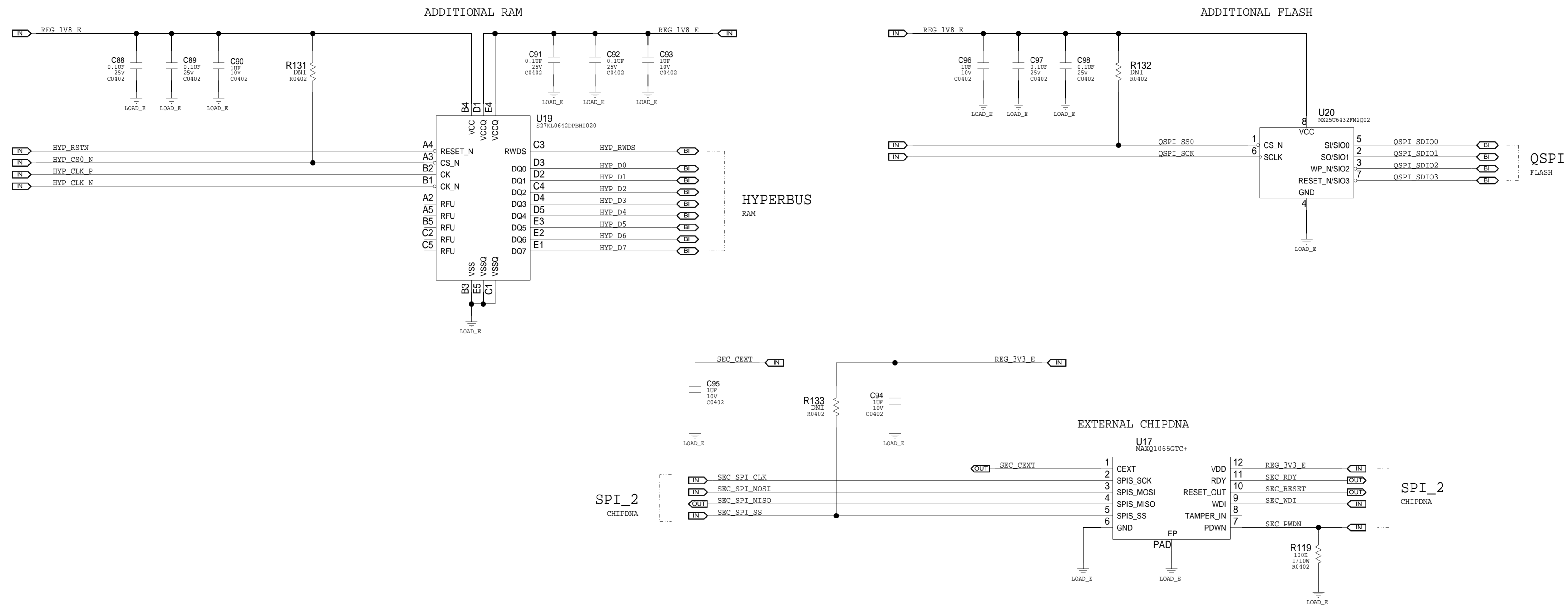


LOAD_N = GROUND IN 1ST BOARD
 LOAD_E = GROUND IN 2ND BOARD
 ISO_GND = GROUND FOR ISOLATED CKT

| SCHEMATIC | | | |
|--------------|---------------|---|-----|
| | | HW TYPE : Customer Evaluation Z Product(s) : MAX32690 : ADFS124-4 | |
| DESIGN VIEW | <DESIGN_VIEW> | DRAWING NO. | REV |
| PTD ENGINEER | D. SUDOY | 02_084576 | B |
| SIZE | SCALE | SHEET | OF |
| D | 1:1 | 4 | 10 |

| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

NETWORK SIGNAL PATH - UCONTROLLER EXTERNAL RAM, FLASH & CHIPDNA

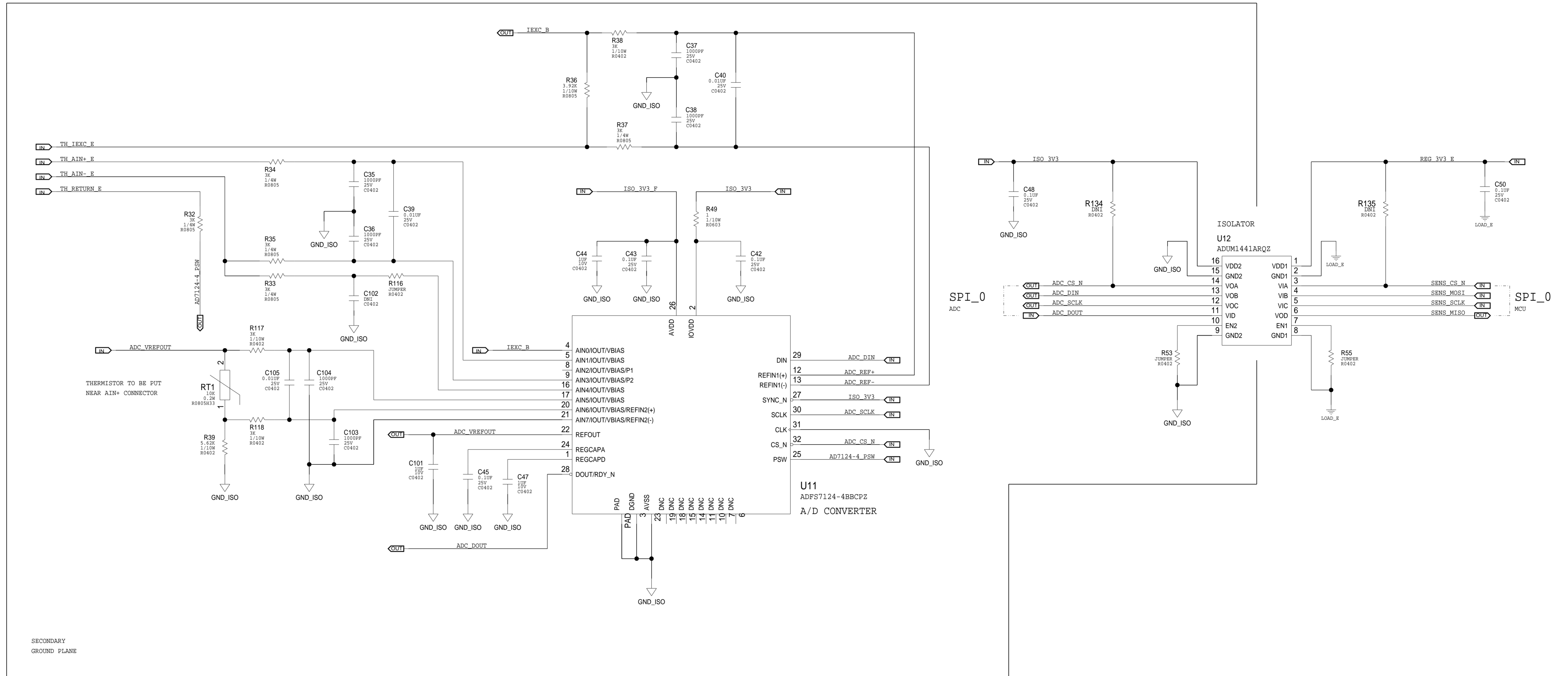


LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

| | | | |
|------------------------------|--|--------------|---------------|
| | SCHEMATIC | | |
| | HW TYPE : Customer Evaluation Z Product(s): MAX32690 : ADFS124-4 | | |
| DESIGN VIEW <DESIGN_VIEW> | DRAWING NO. 02_084576 | REV B | SHEET 5 OF 10 |
| PTD ENGINEER D. SUDOY | SIZE D | SCALE 1:1 | SHEET 5 OF 10 |

SENSOR SIGNAL AND ISOLATION PATH

| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

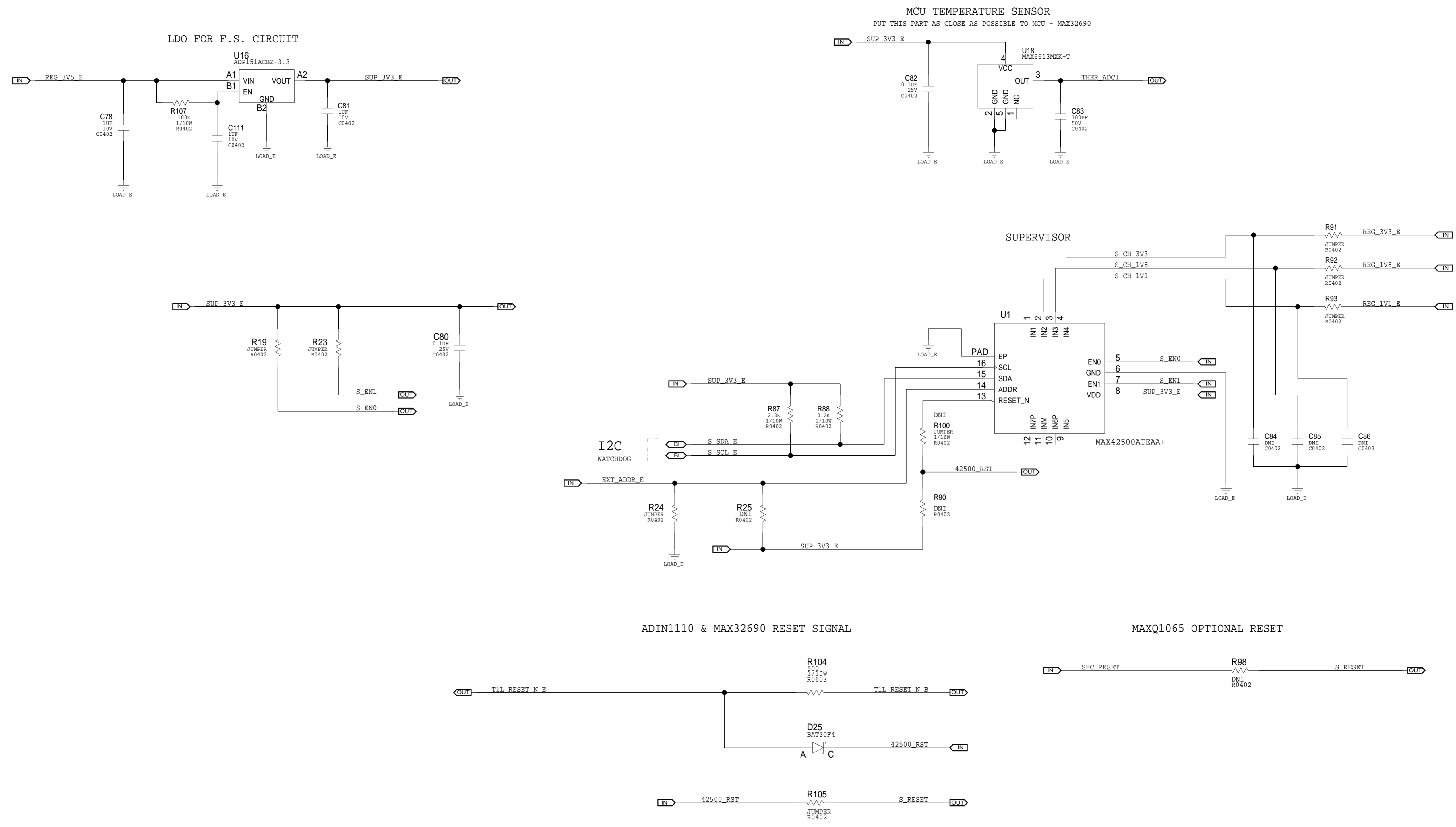


LOAD_N = GROUND IN 1ST BOARD
 LOAD_E = GROUND IN 2ND BOARD
 ISO_GND = GROUND FOR ISOLATED CKT

| ANALOG DEVICES | | SCHEMATIC | |
|--|--------------------------|------------------------------|---------------|
| HW TYPE : Customer Evaluation Z Product(s) : MAX32690 : ADFS7124-4 | | DESIGN VIEW <DESIGN_VIEW> | REV B |
| PTD ENGINEER D. SUDOY | DRAWING NO. 02_084576 | SCALE 1:1 | SHEET 6 OF 10 |

| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

SUPERVISORY CIRCUIT - MAX42500



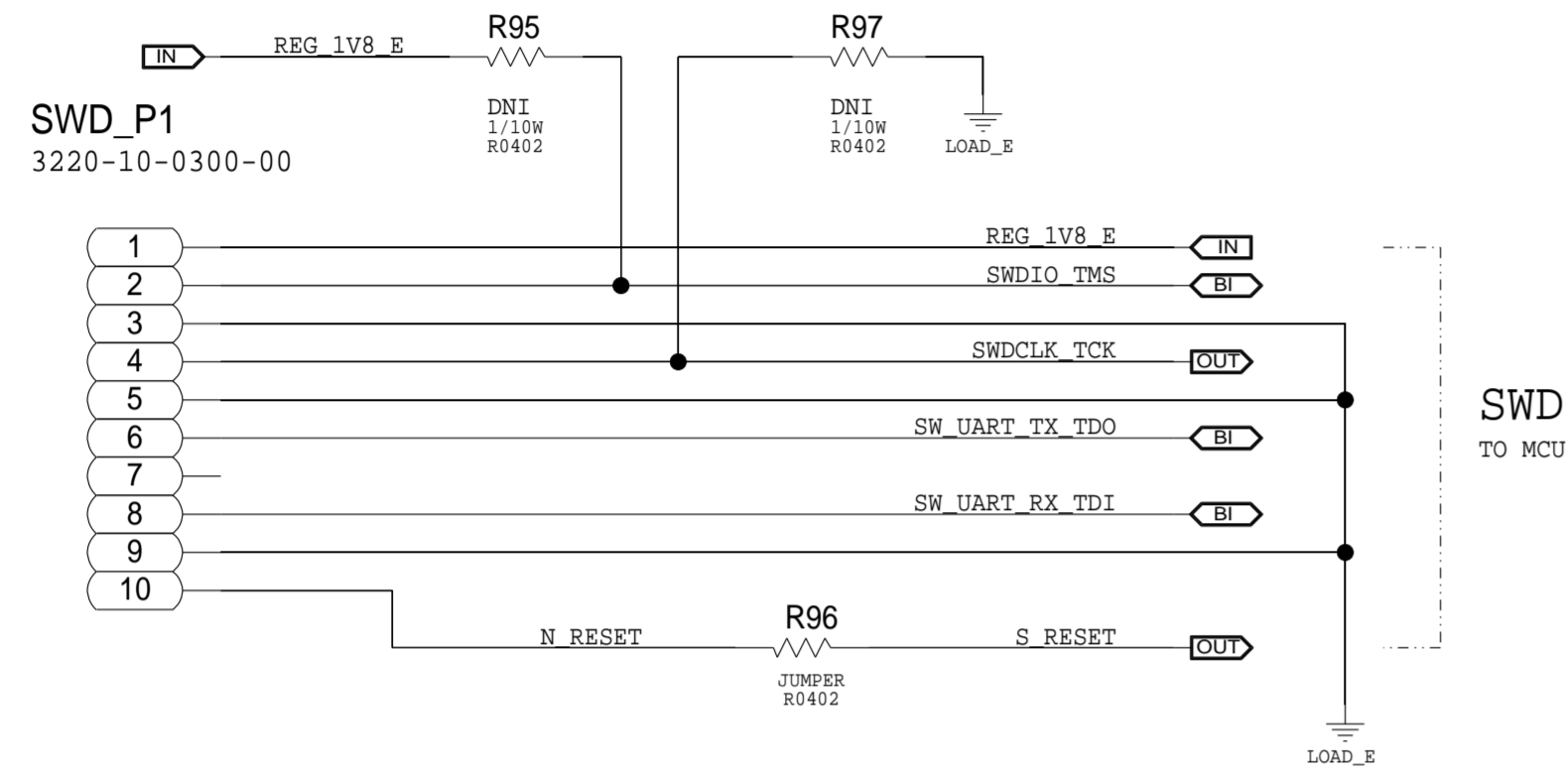
LOAD_N = GROUND IN 1ST BOARD
 LOAD_E = GROUND IN 2ND BOARD
 ISO_GND = GROUND FOR ISOLATED CKT

| | | | |
|------------------------------|--|--------------|---------------|
| | SCHEMATIC | | |
| | HW TYPE : Customer Evaluation Z Product(s) : MAX32690 : ADFS124-4 | | |
| DESIGN VIEW <DESIGN_VIEW> | DRAWING NO. 02_084576 | REV B | |
| PTD ENGINEER D. SUDOY | SIZE D | SCALE 1:1 | SHEET 7 OF 10 |

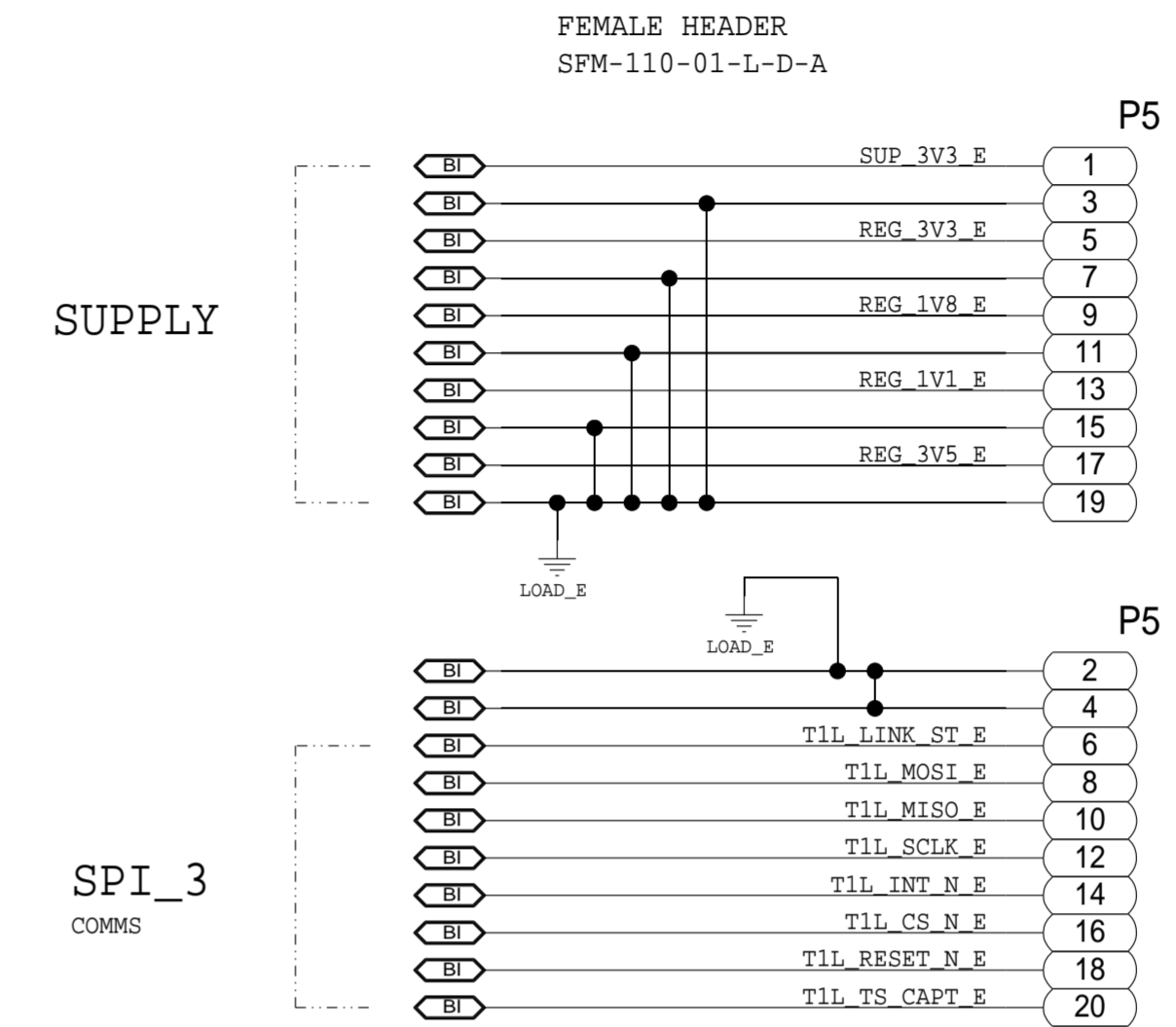
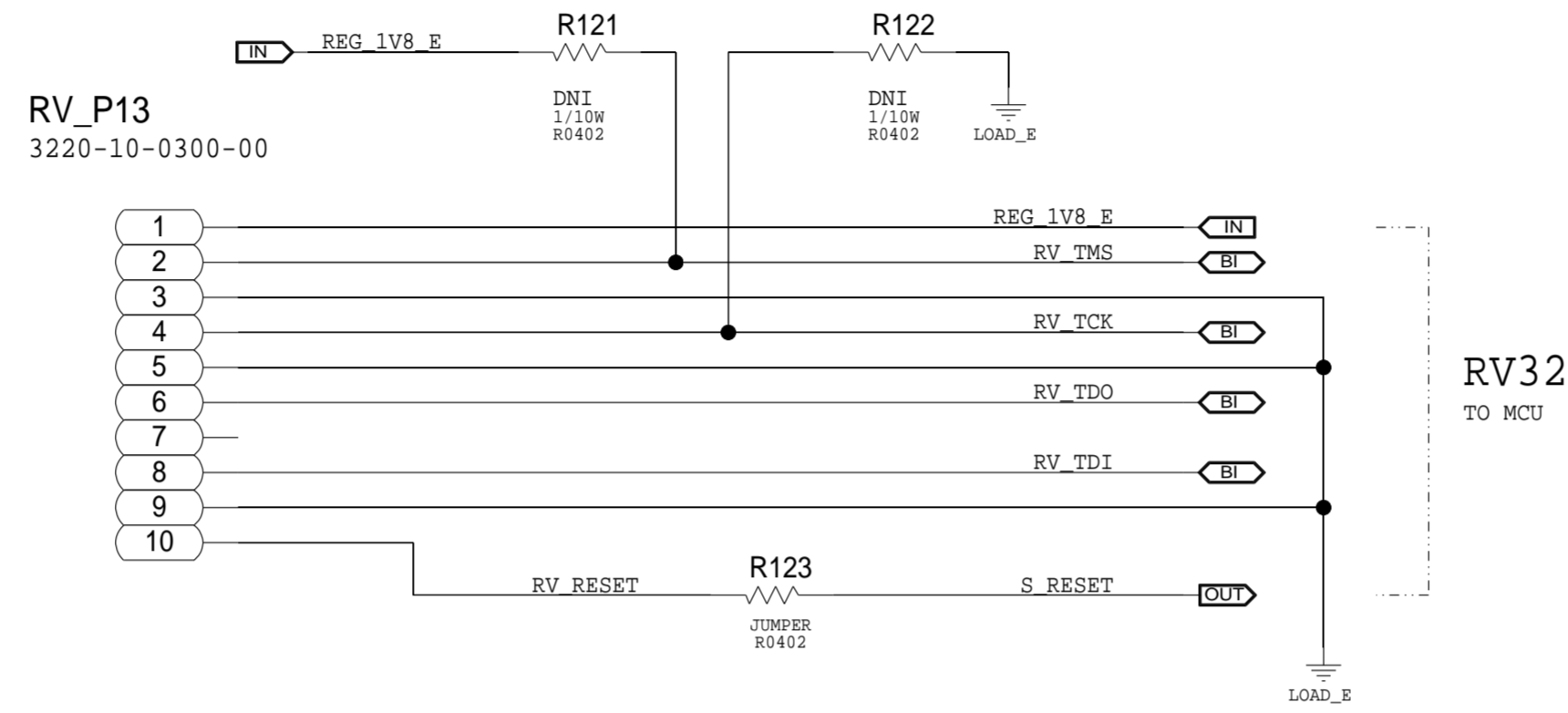
| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

CONNECTORS

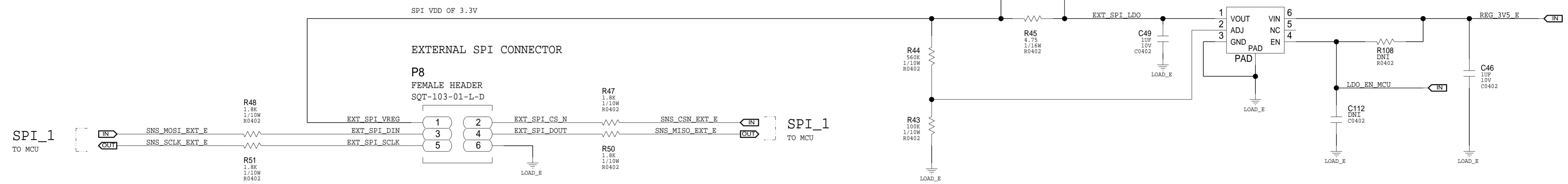
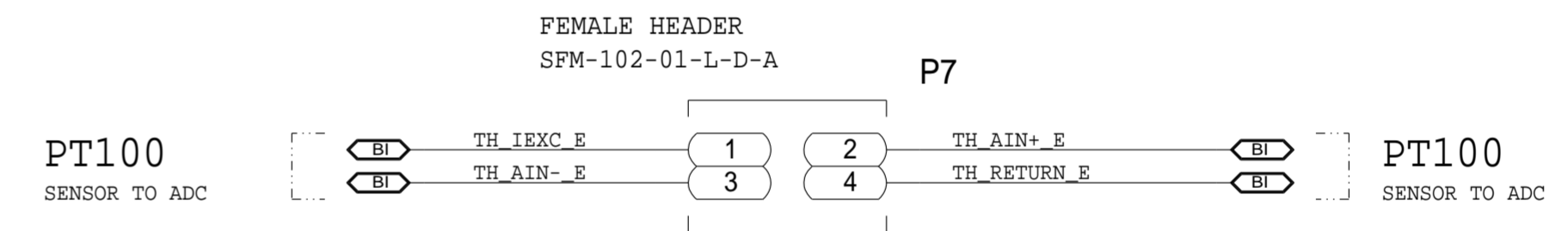
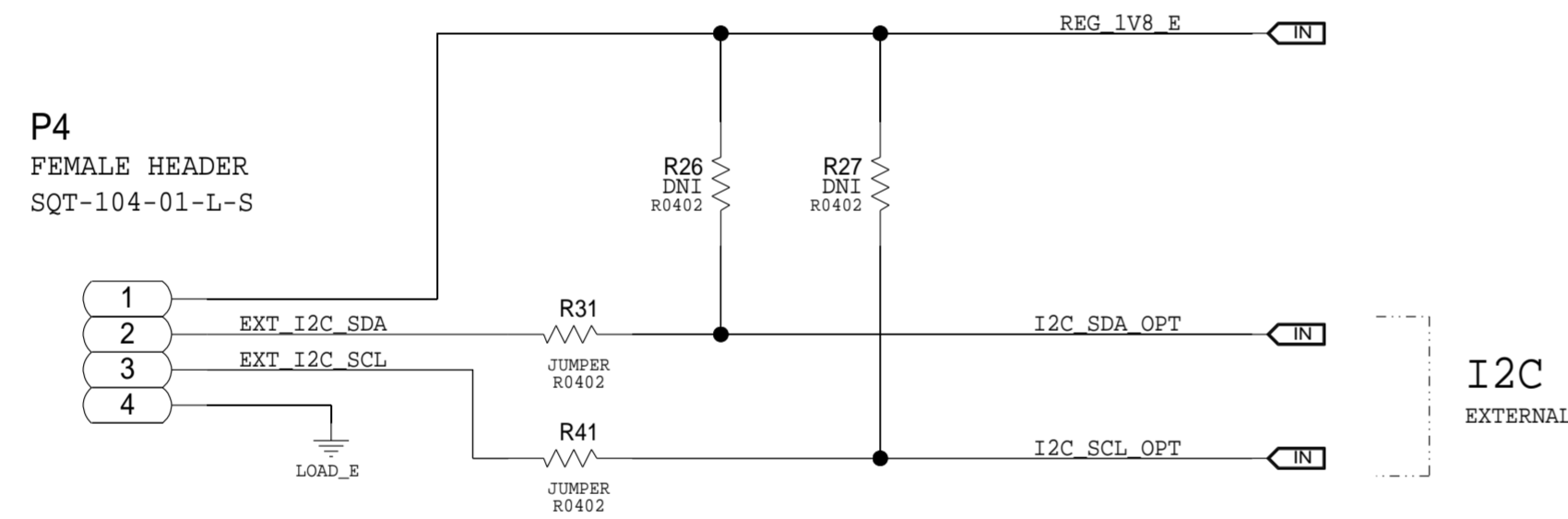
SWD/PROGRAMMER CONNECTOR
FOR DEVELOPMENT ONLY



RV JTAG CONNECTOR
FOR DEVELOPMENT ONLY



I2C CONNECTION FOR
USER DEVELOPMENT/DEBUGGING ONLY



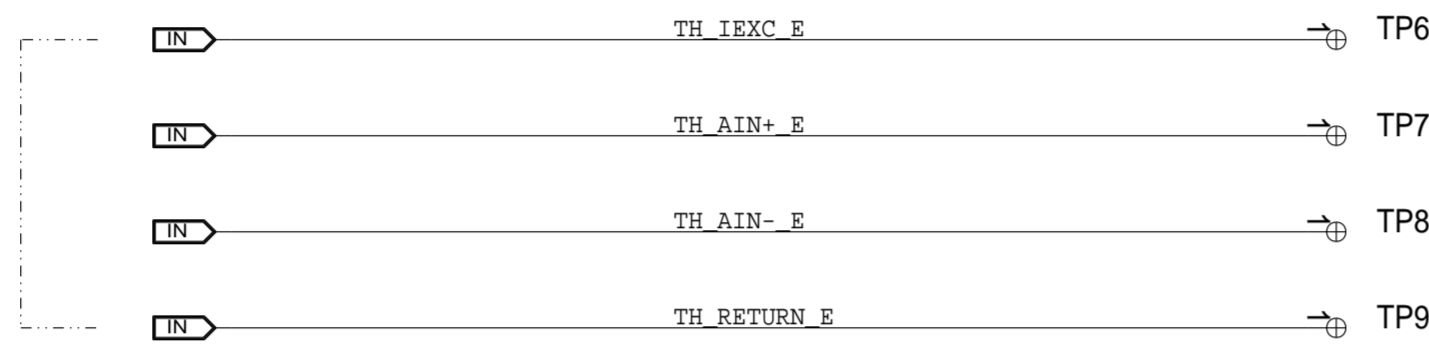
LOAD_N = GROUND IN 1ST BOARD
LOAD_E = GROUND IN 2ND BOARD
ISO_GND = GROUND FOR ISOLATED CKT

| ANALOG DEVICES | | SCHEMATIC | |
|---------------------------------|--|-----------------------|-----------|
| HW TYPE : Customer Evaluation Z | | Product(s) : MAX32690 | |
| DESIGN VIEW <DESIGN_VIEW> | | DRAWING NO. 02_084576 | REV B |
| PTD ENGINEER D. SUDOY | | SIZE D | SCALE 1:1 |
| | | SHEET 8 | OF 10 |

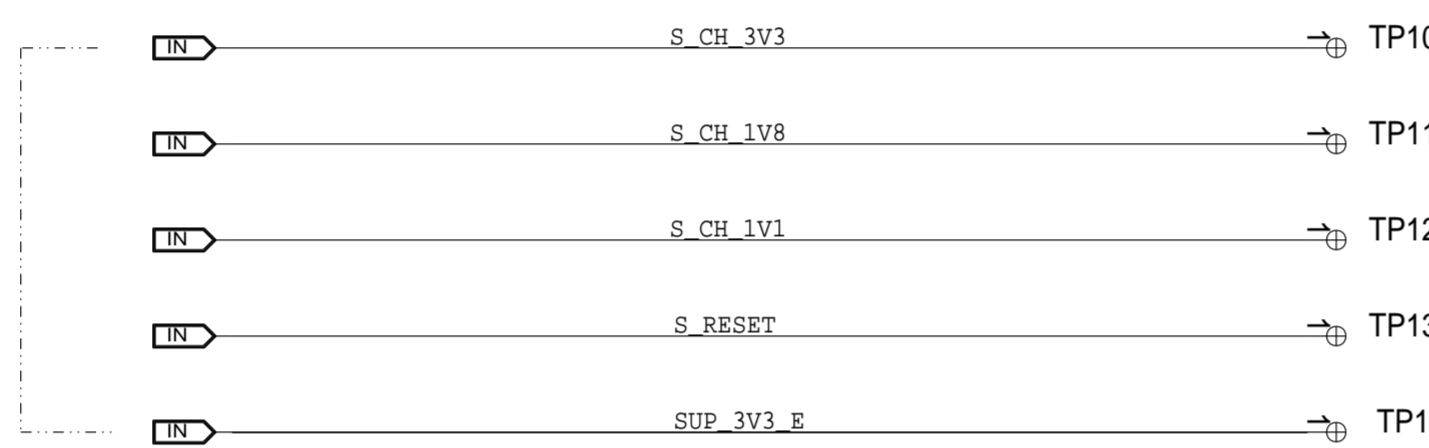
| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

TEST POINTS

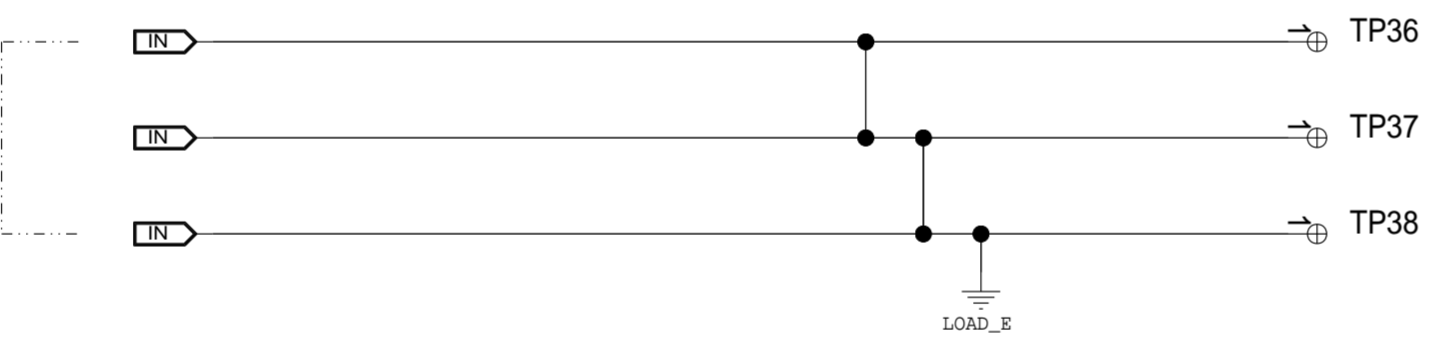
TEMPERATURE SENSOR



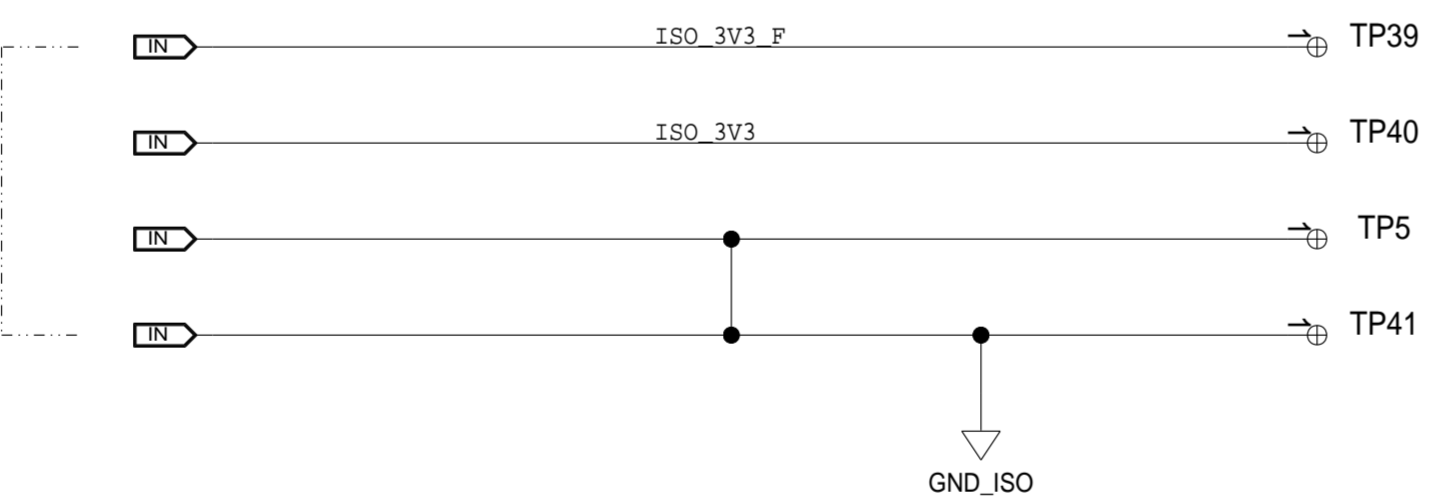
SUPERVISOR



GROUNDING

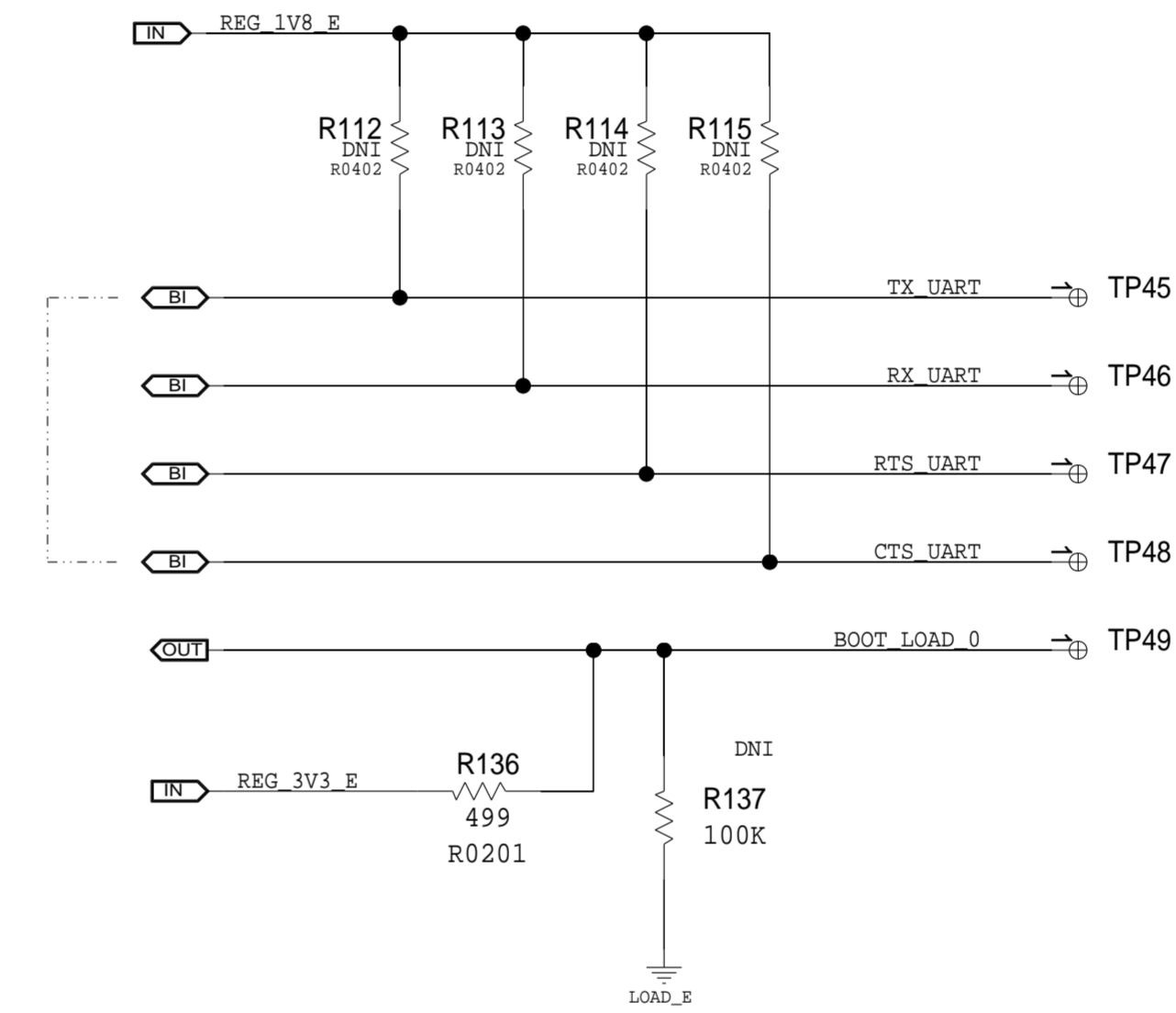


ISOLATED POWER



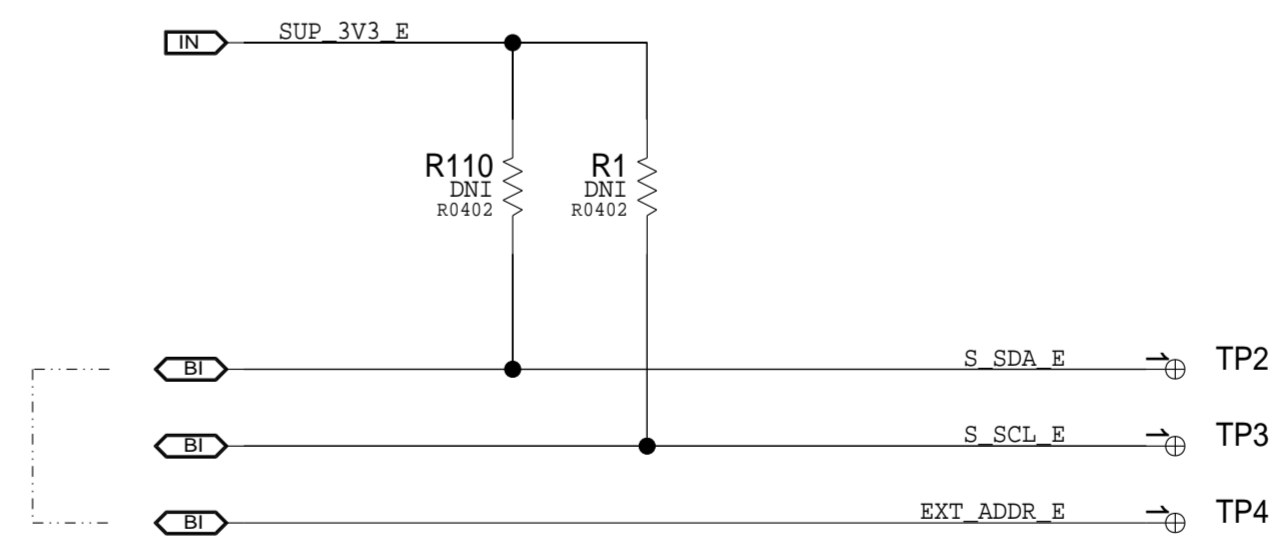
UART

TESTPADS AS OPTIONAL PINS



I2C

MAX42500
TESTPADS AS OPTIONAL PINS

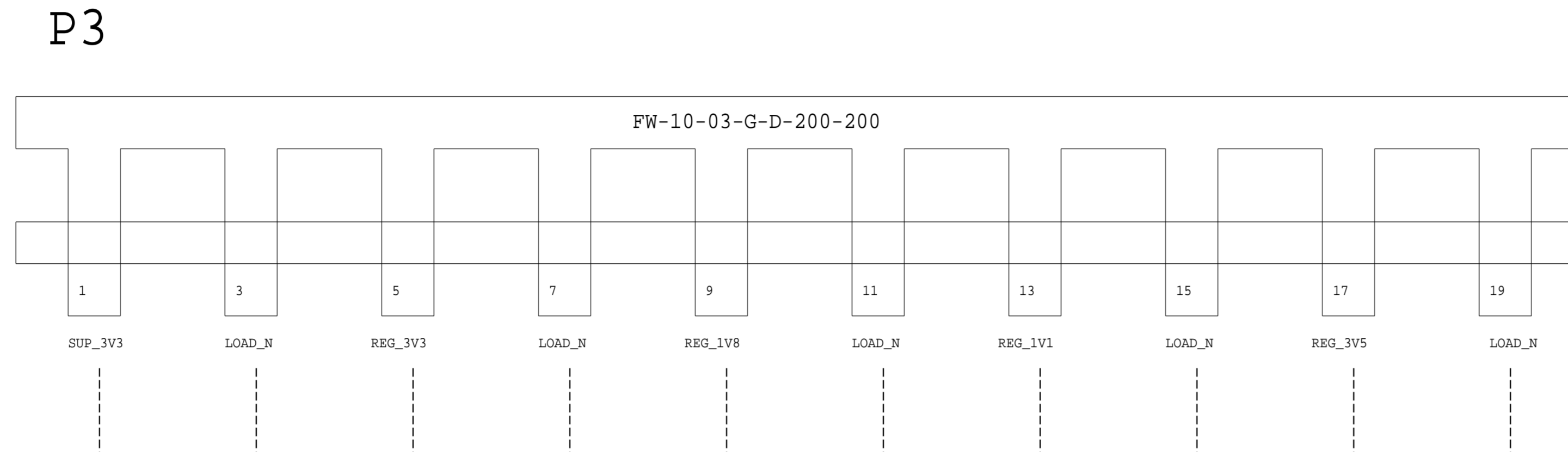


| | | | |
|------------------------------|---|--------------|---------------|
| | SCHEMATIC | | |
| | HW TYPE : Customer Evaluation Z Product(s): MAX32690 : ADFS7124-4 | | |
| DESIGN VIEW <DESIGN_VIEW> | DRAWING NO. 02_084576 | REV B | |
| PTD ENGINEER D. SUDOY | SIZE D | SCALE 1:1 | SHEET 9 OF 10 |

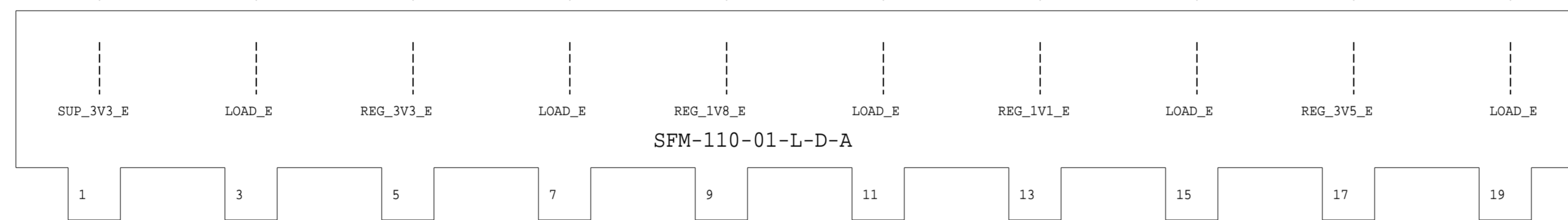
| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

INTERCONNECTION OF TWO BOARDS

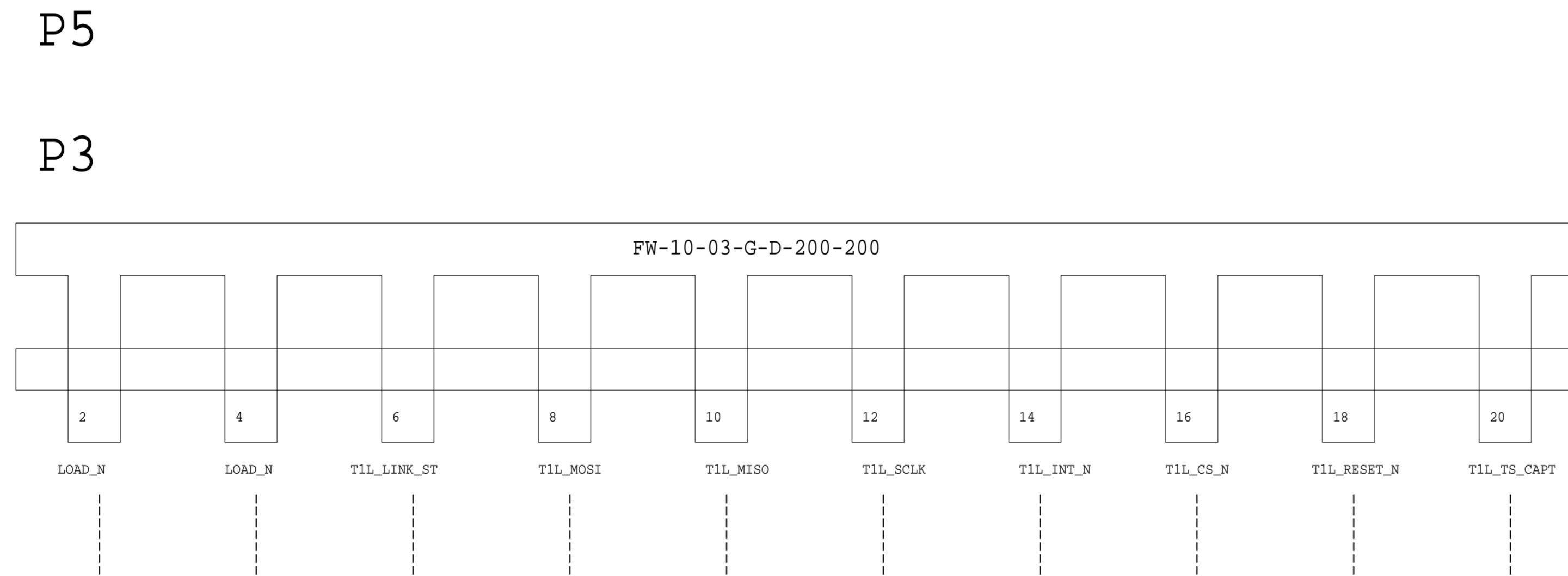
PCB-1
ODD PINS



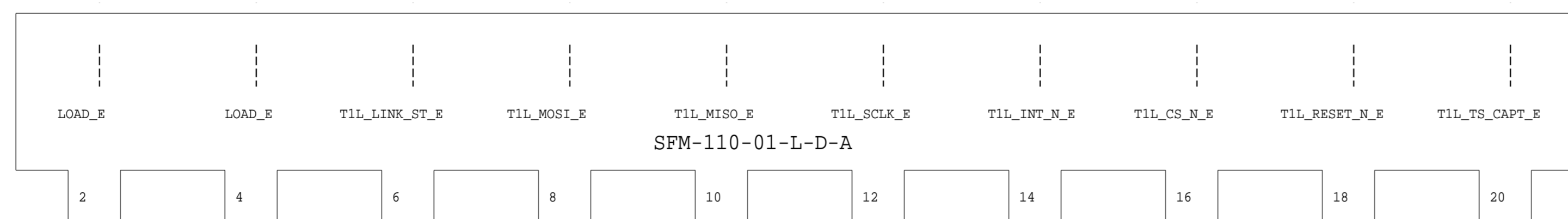
PCB-2
ODD PINS



PCB-1
EVEN PINS



PCB-2
EVEN PINS



P5

P7

| | | | |
|---|---|--------------------------|--------------|
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| | HW TYPE : Customer Evaluation Z Product(s): MAX32690 : ADFS7124-4 | | |
| | DESIGN VIEW <DESIGN_VIEW> | DRAWING NO. 02_084576 | REV B |
| | PTD ENGINEER D. SUDOY | SIZE D | SCALE 1:1 |
| | | SHEET 10 OF 10 | |