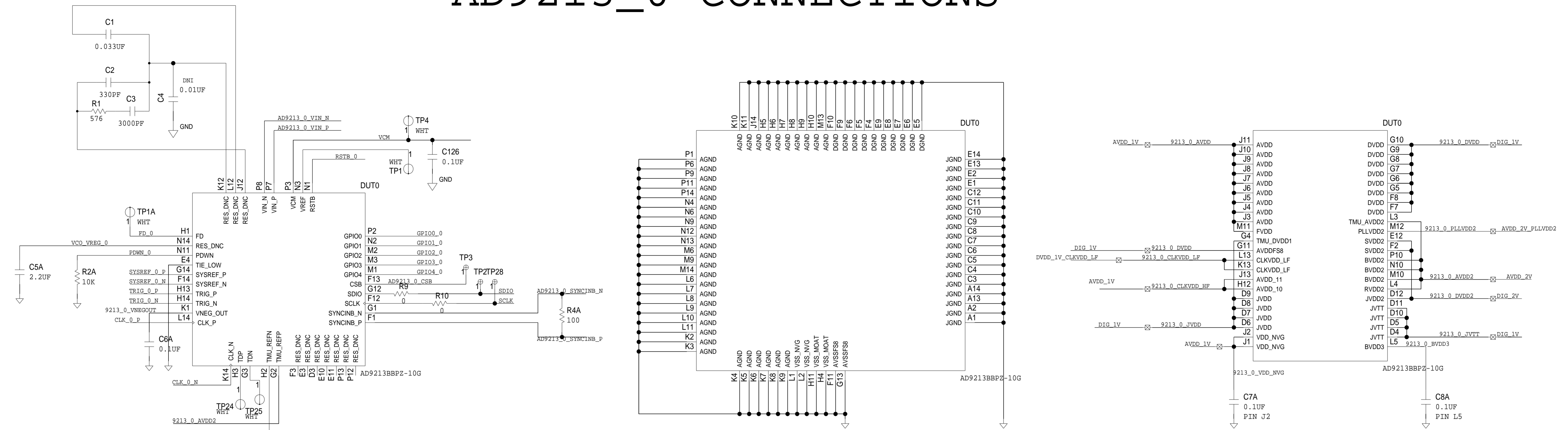
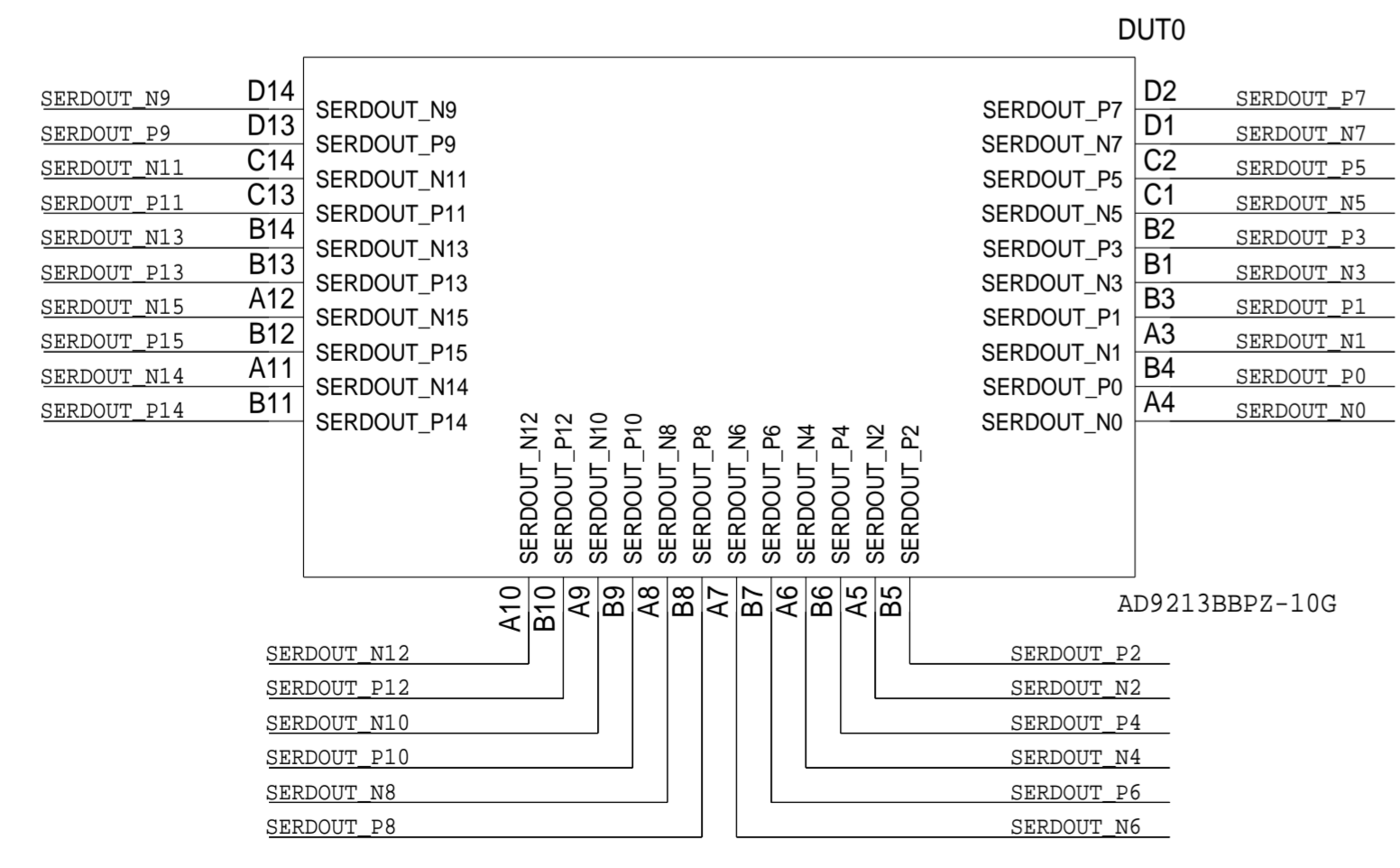
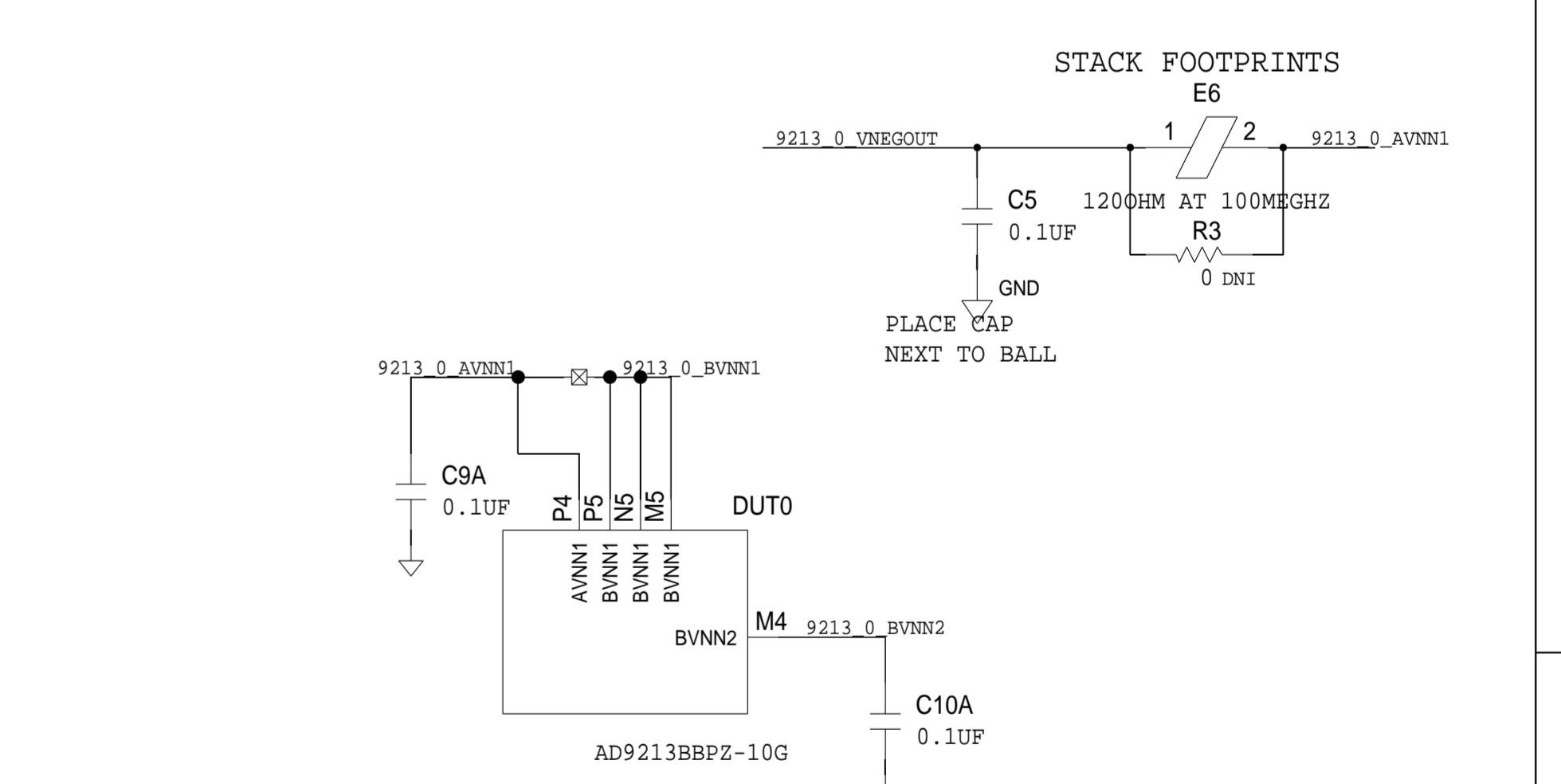


AD9213_0 CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SERDOUT_N	Capacitor	DOUT_N
SERDOUT_N0	C11A	DOUT_N0
SERDOUT_P0	C12A 0.1UF	DOUT_P0
SERDOUT_N1	0.1UF C13A	DOUT_N1
SERDOUT_P1	C14A 0.1UF	DOUT_P1
SERDOUT_N2	0.1UF C15A	DOUT_N2
SERDOUT_P2	C16A 0.1UF	DOUT_P2
SERDOUT_N3	0.1UF C17A	DOUT_N3
SERDOUT_P3	C18A 0.1UF	DOUT_P3
SERDOUT_N4	0.1UF C19A	DOUT_N4
SERDOUT_P4	C20A 0.1UF	DOUT_P4
SERDOUT_N5	0.1UF C21A	DOUT_N5
SERDOUT_P5	C22A 0.1UF	DOUT_P5
SERDOUT_N6	0.1UF C23A	DOUT_N6
SERDOUT_P6	C24A 0.1UF	DOUT_P6
SERDOUT_N7	0.1UF C25A	DOUT_N7
SERDOUT_P7	C26A 0.1UF	DOUT_P7
SERDOUT_N8	0.1UF	DOUT_N8
SERDOUT_P8	C27A	DOUT_P8
SERDOUT_N9	0.1UF C29A	DOUT_N9
SERDOUT_P9	C30A 0.1UF	DOUT_P9
SERDOUT_N10	0.1UF C31A	DOUT_N10
SERDOUT_P10	C32A 0.1UF	DOUT_P10
SERDOUT_N11	0.1UF C33A	DOUT_N11
SERDOUT_P11	C34A 0.1UF	DOUT_P11
SERDOUT_N12	0.1UF C35A	DOUT_N12
SERDOUT_P12	C36A 0.1UF	DOUT_P12
SERDOUT_N13	0.1UF C37A	DOUT_N13
SERDOUT_P13	C38A 0.1UF	DOUT_P13
SERDOUT_N14	0.1UF C39A	DOUT_N14
SERDOUT_P14	C40A 0.1UF	DOUT_P14
SERDOUT_N15	0.1UF C41A	DOUT_N15
SERDOUT_P15	C42A 0.1UF	DOUT_P15



	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D	
PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1	SHEET 2 OF 13

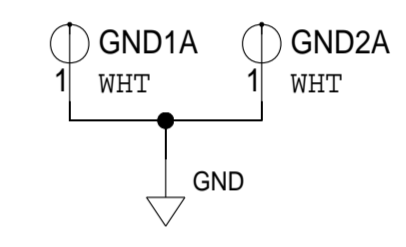
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN ANY MANNER THAT WOULD BE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

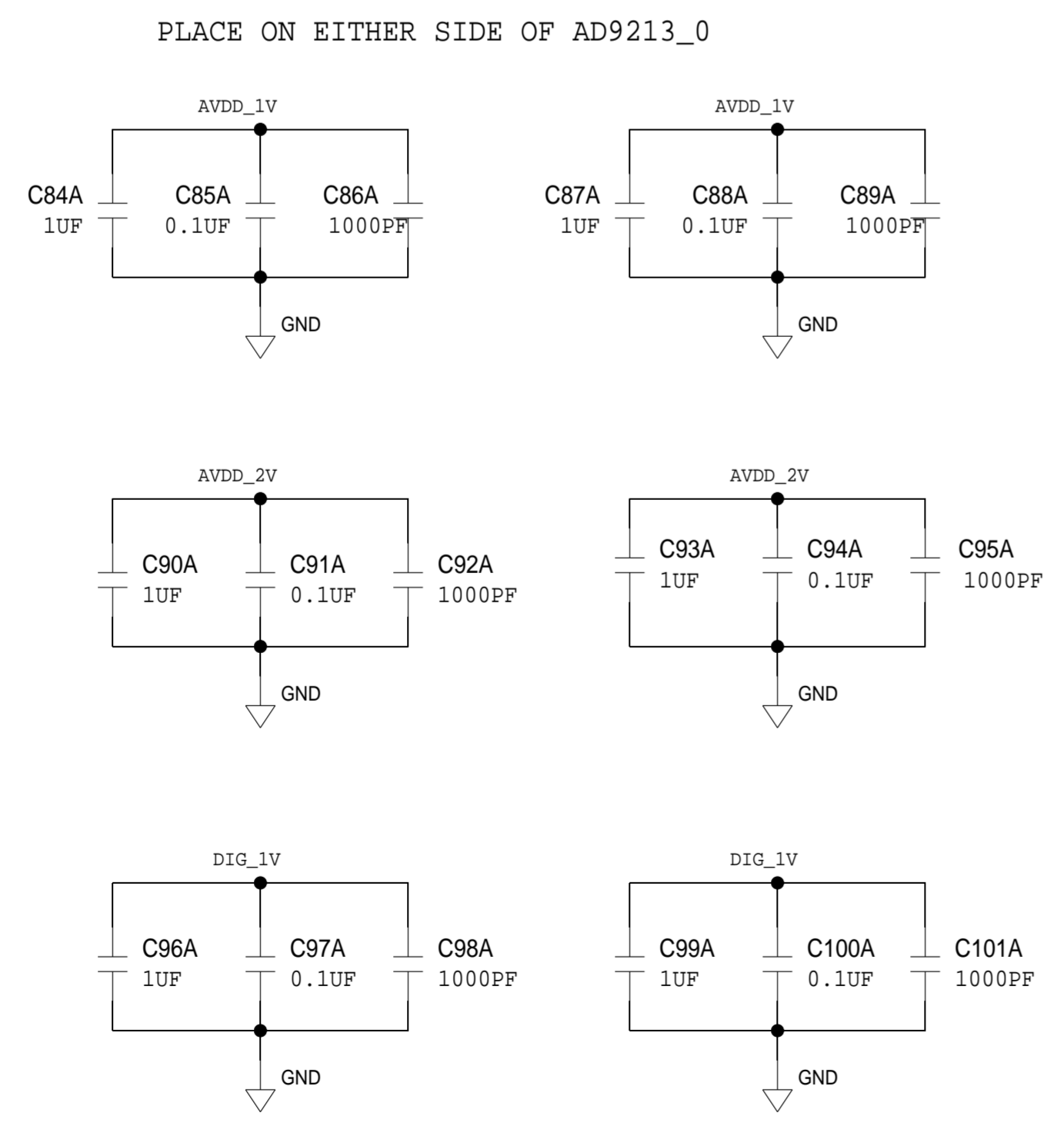
DECOUPLING CAPACITORS DIRECTLY BELOW BGA FOOTPRINT FOR AD9213_0



DISTRIBUTED GND TEST PTS



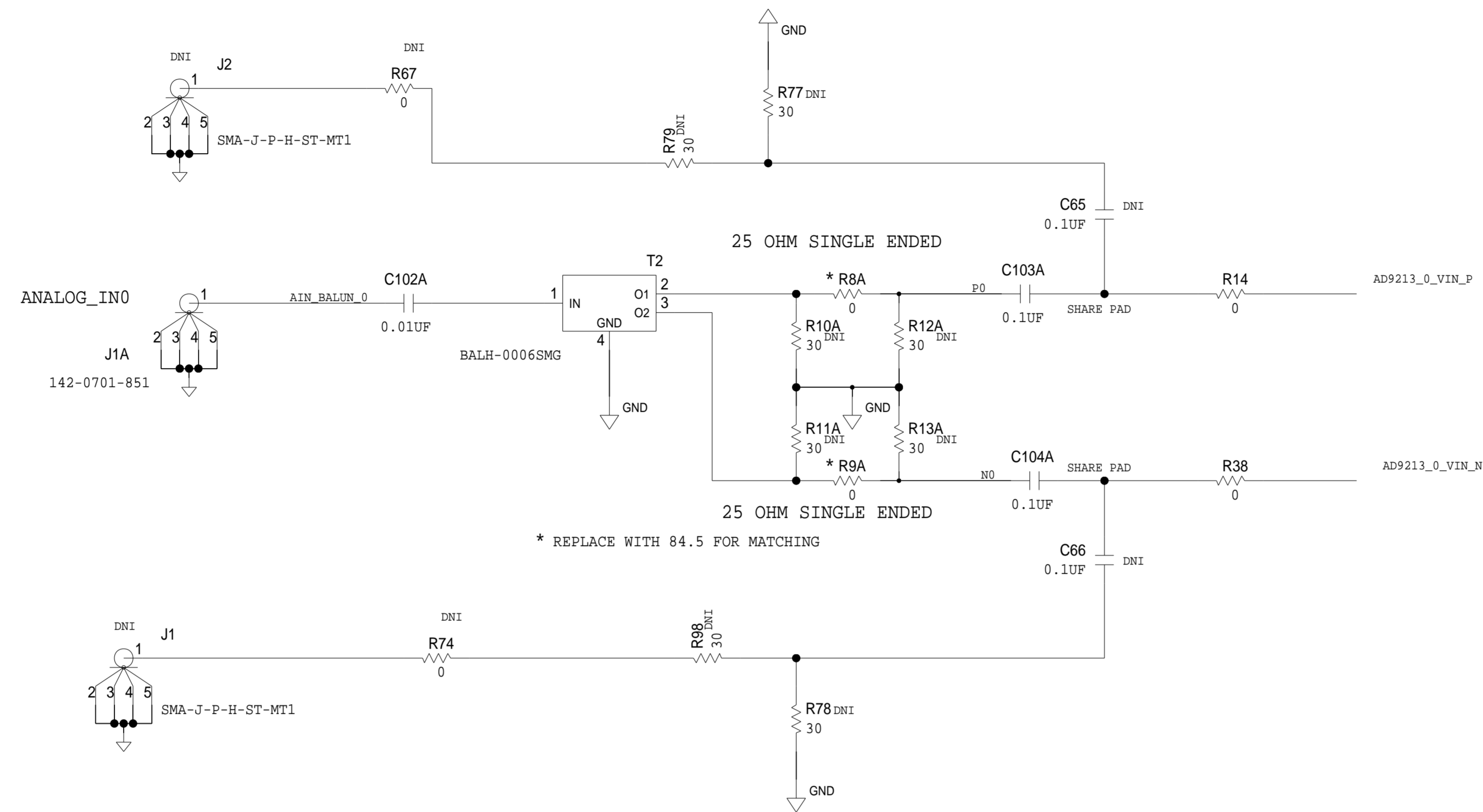
DISTRIBUTED DECOUPLING CAPACITORS



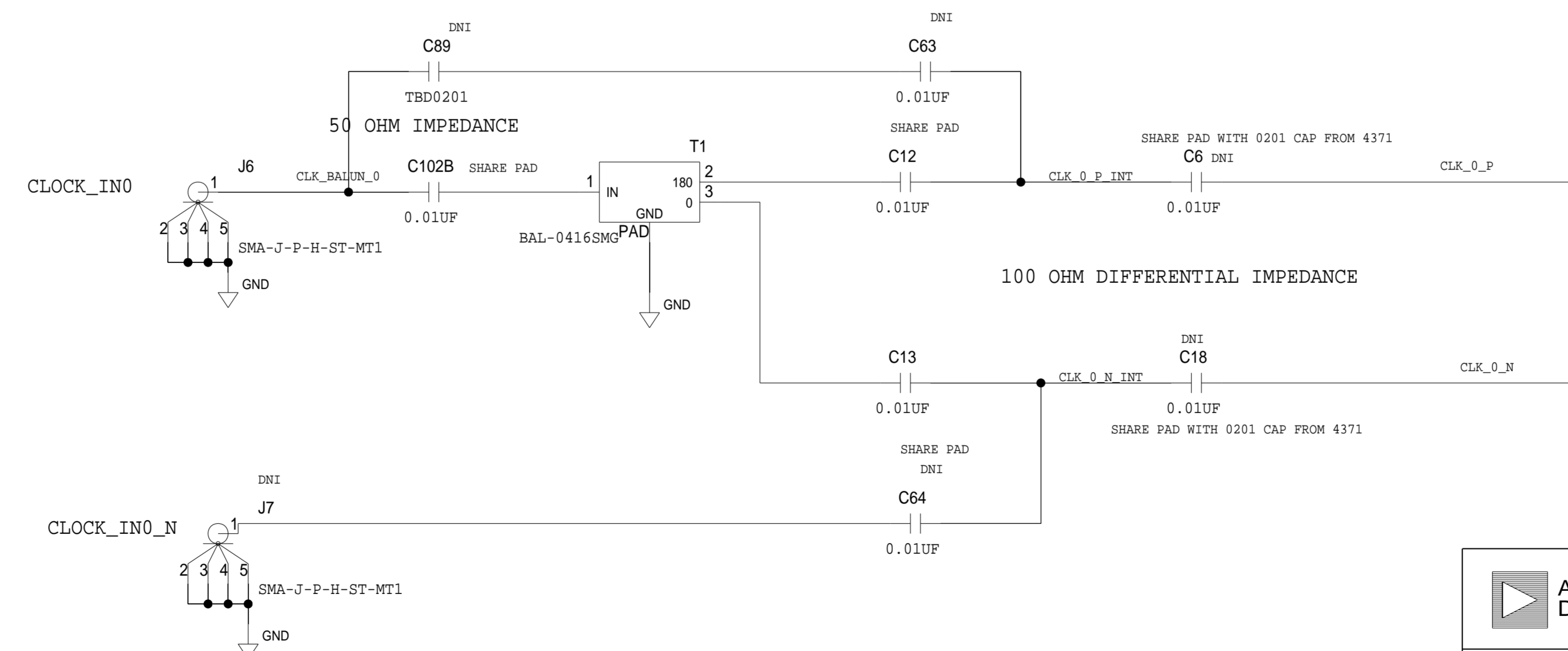
	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D
PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1	SHEET 3 OF 13

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG FRONT END



CLOCK FRONT END

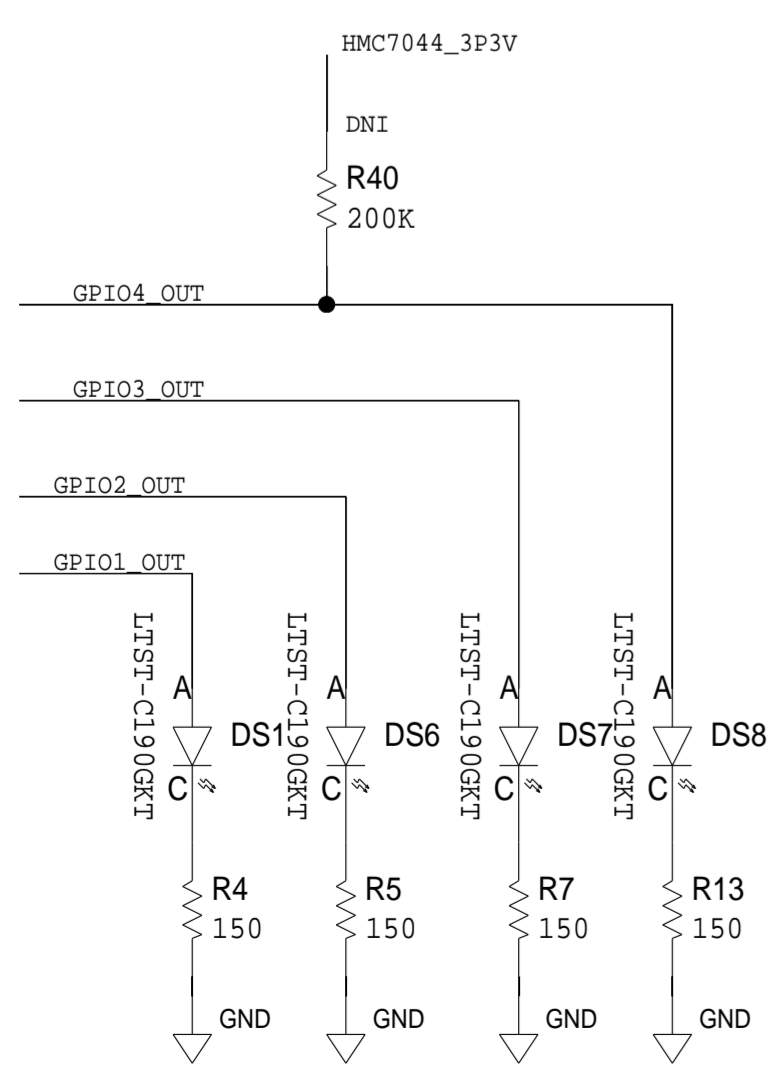
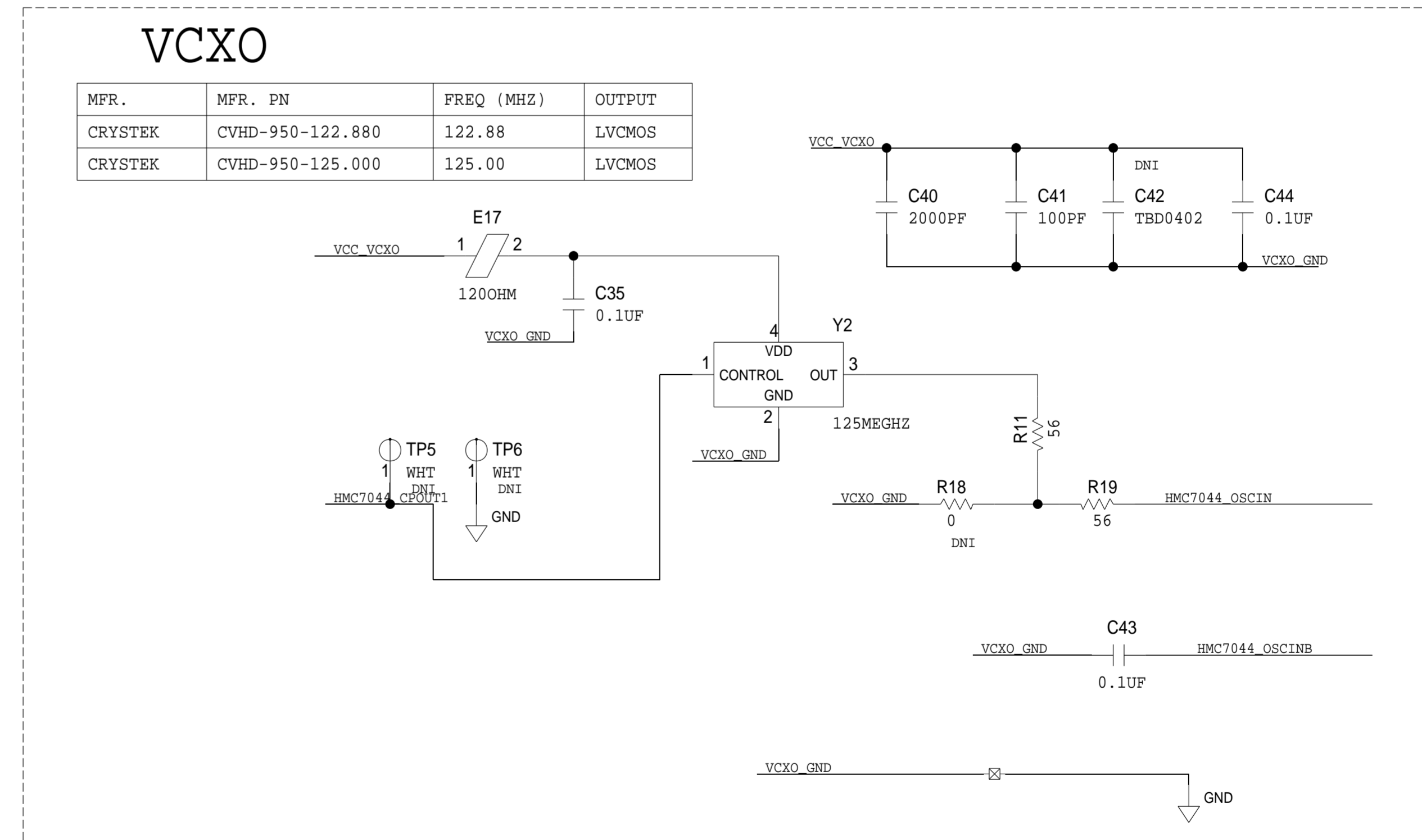
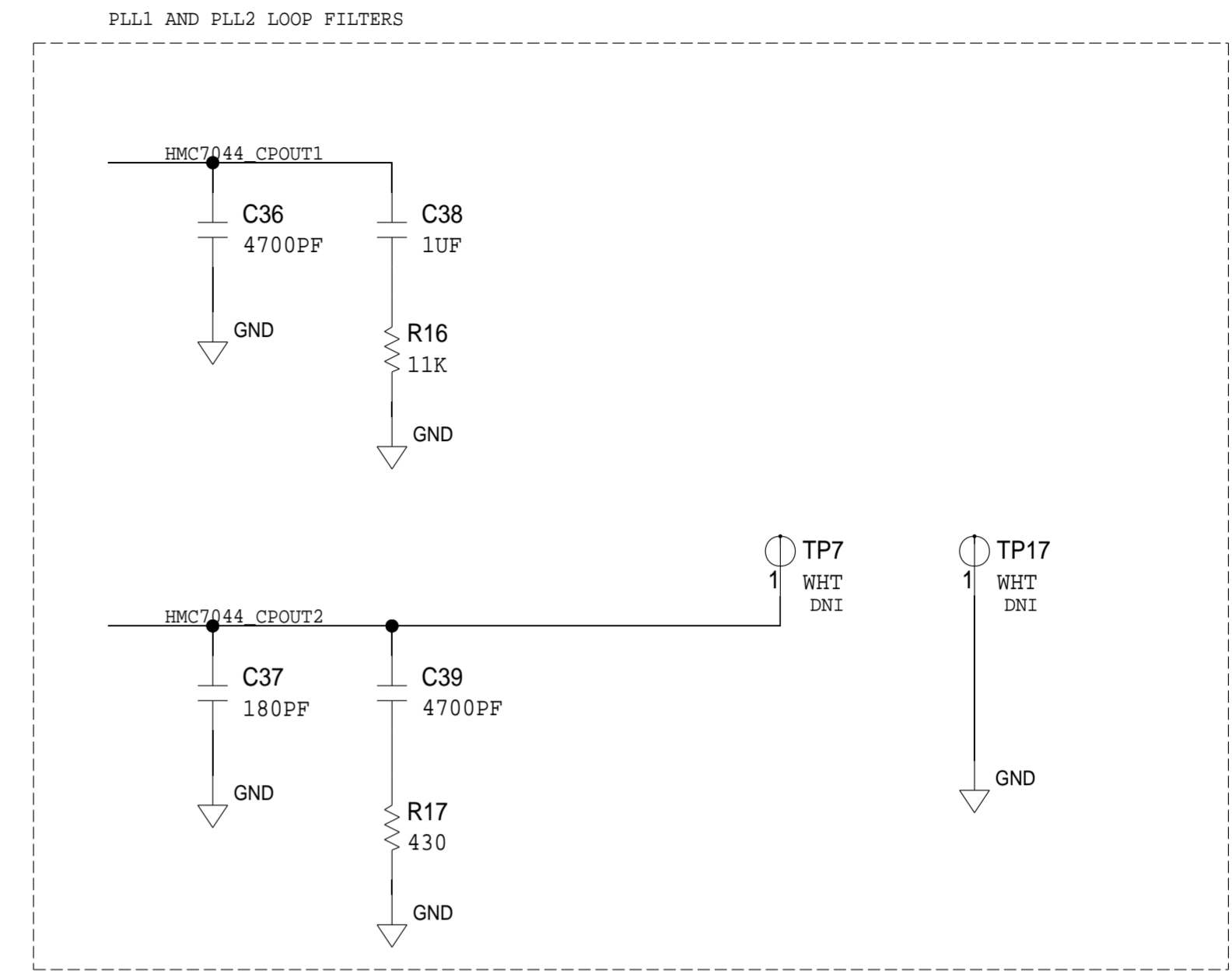
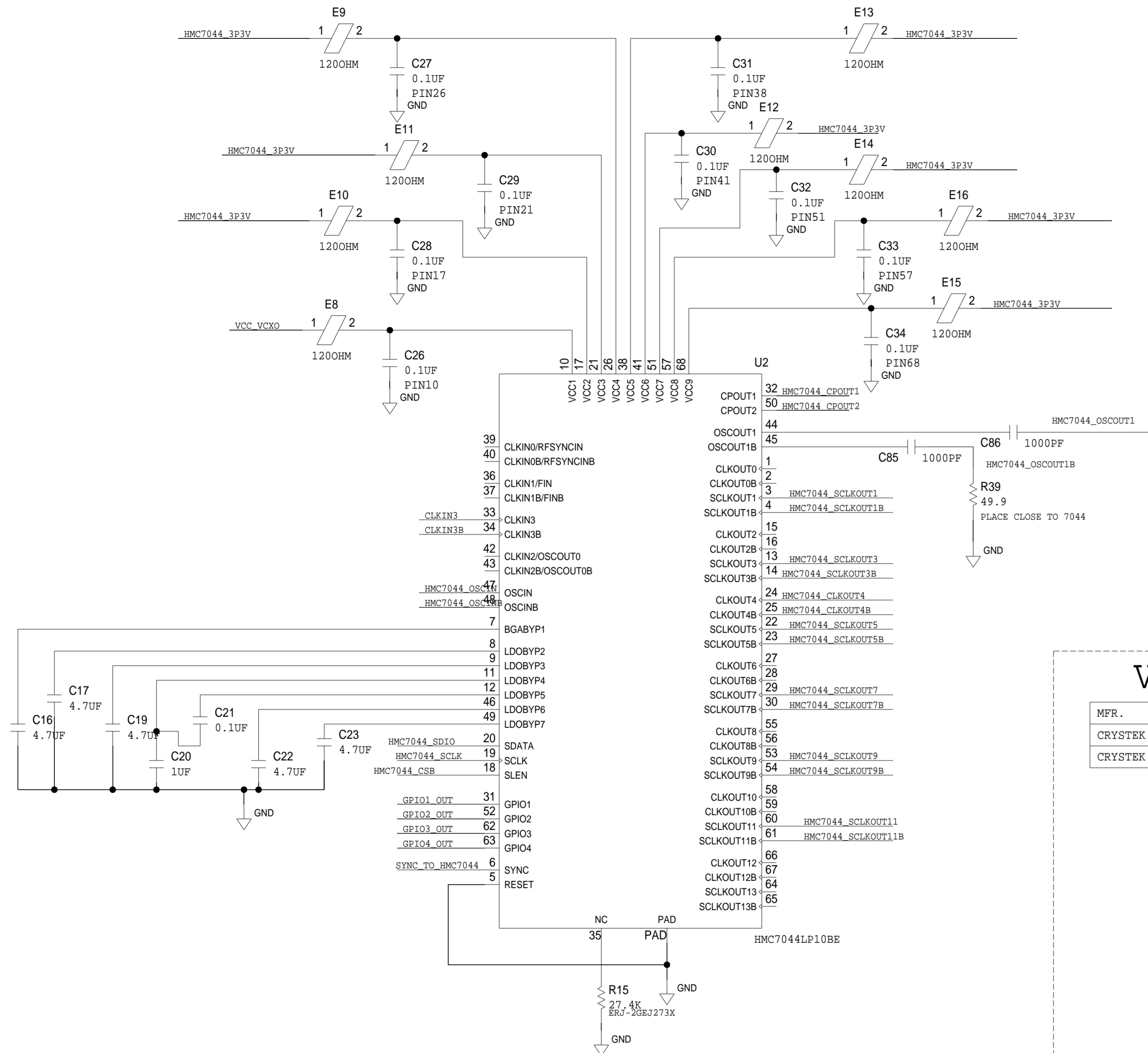


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	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D
	PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1

SHEET 4 OF 13

HMC7044 SUPPLY CONNECTIONS AND VCO

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

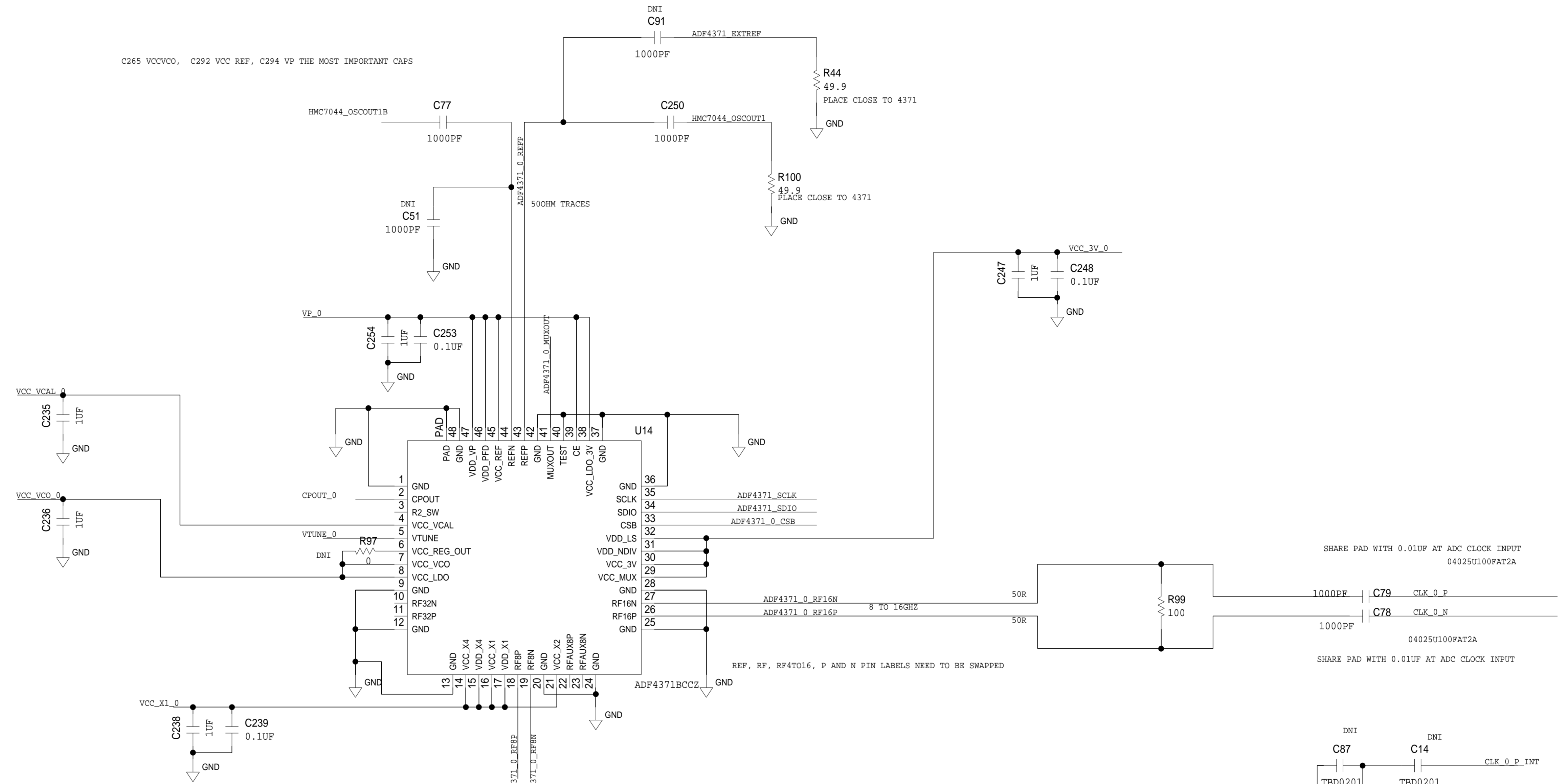


	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D
	PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1

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ADF4371_0

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

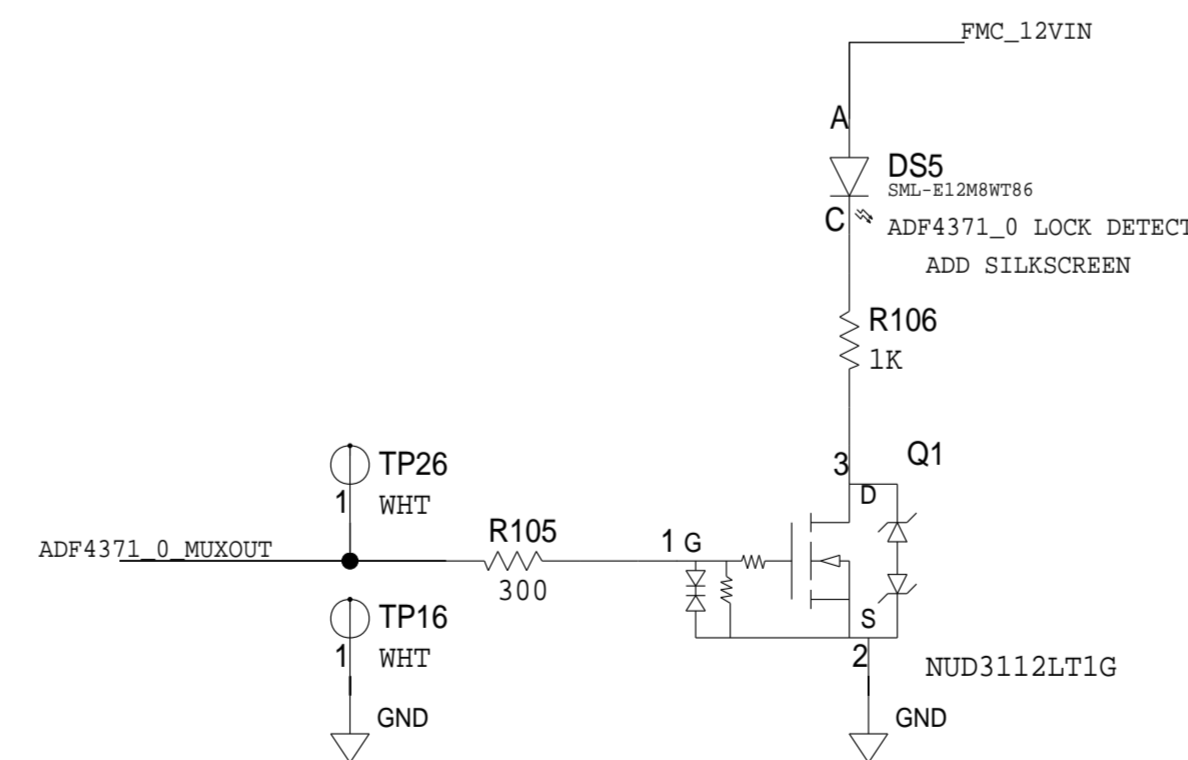
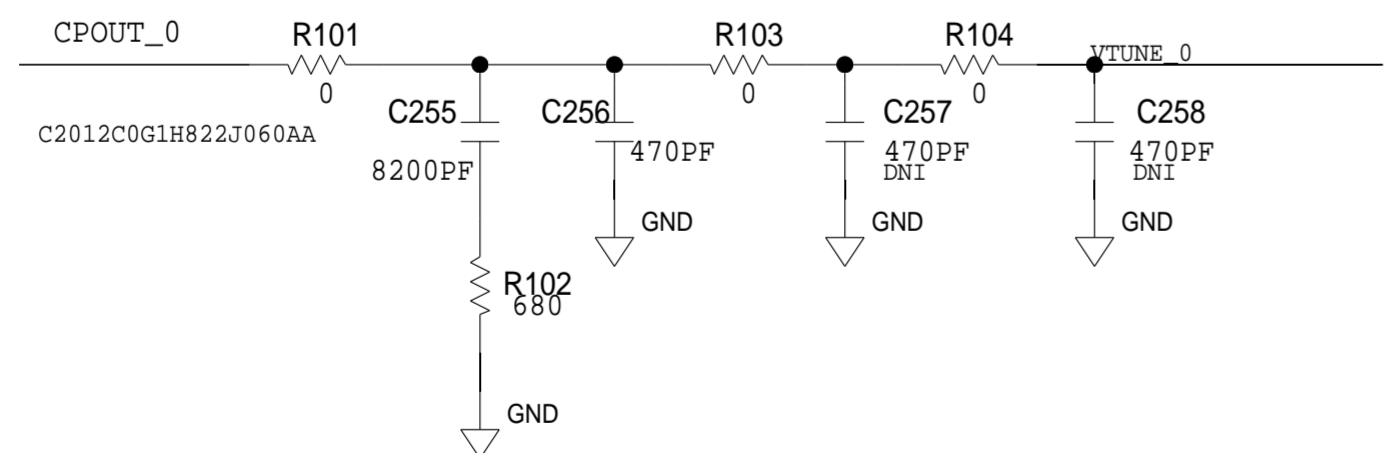


LOOP FILTER VALUES

REF DES	VALUE	P/N
R101	0	MC0603WG00000T5E-TC
C255	8.2NF	C2012COG1H822J060AA
R102	680	ERJ-2RKF6800X
C256	470PF	2238 867 15471
R103	0	MC 0.063W 0603 1% 300R
C257	DNI	
R104	0	MC0603WG00000T5E-TC
C258	DNI	

NOTE: R101= 910HMS IF FRACTIONAL MODE

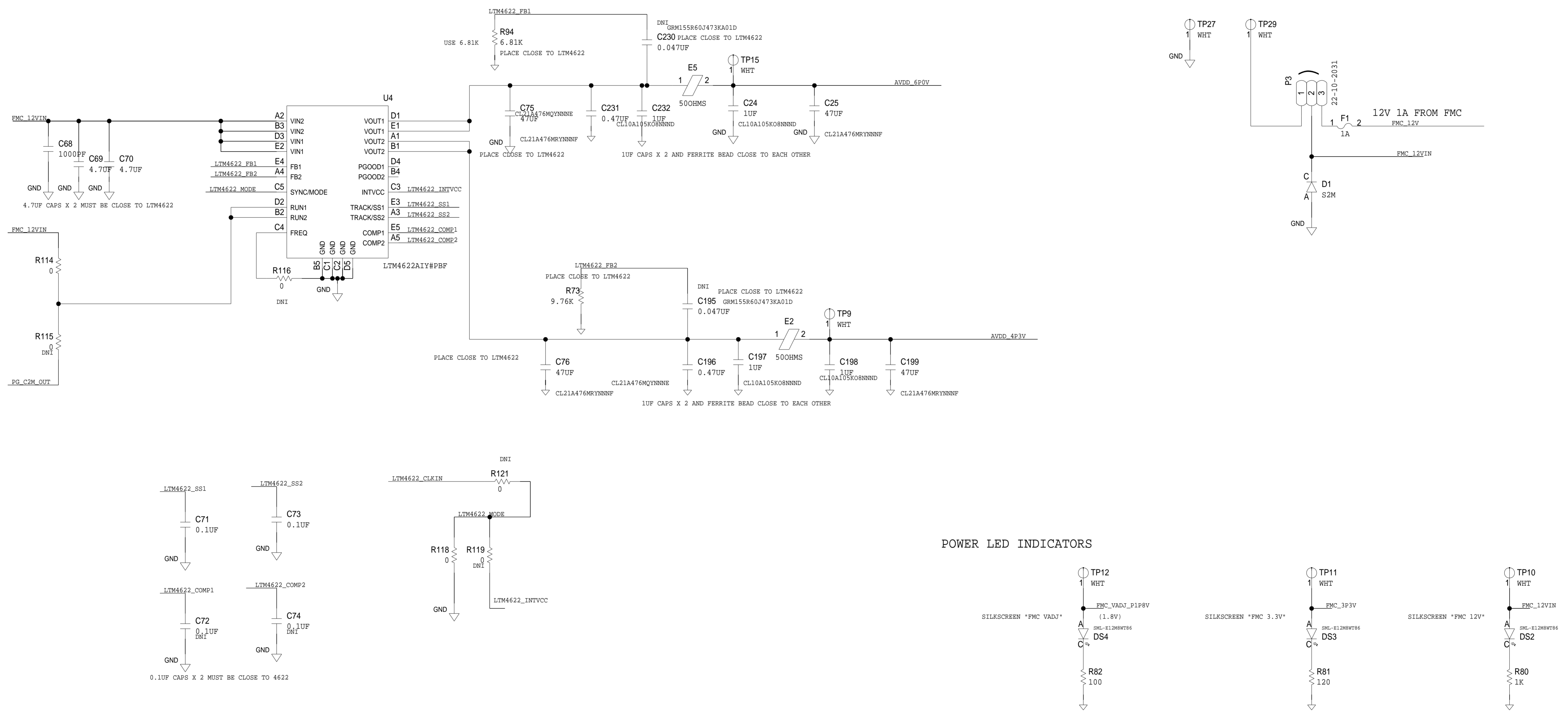
PLACE R101 AS CLOSE AS POSSIBLE TO CPOUT PIN.
 PLACE R102 AND R104 CLOSE TO VTUNE PIN.
 PLACE R103 AND R104 CLOSE TO VTUNE PIN



	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D	
PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1	SHEET 7 OF 13

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

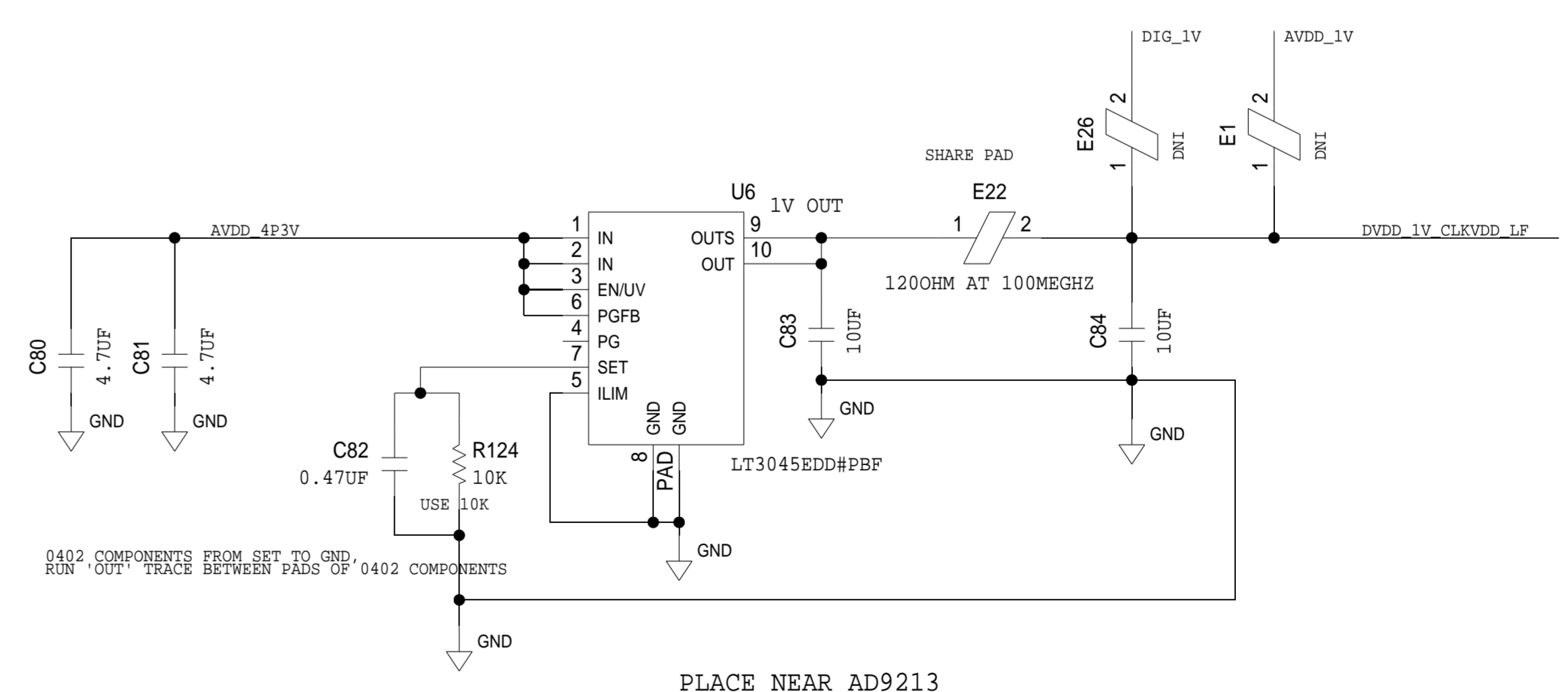
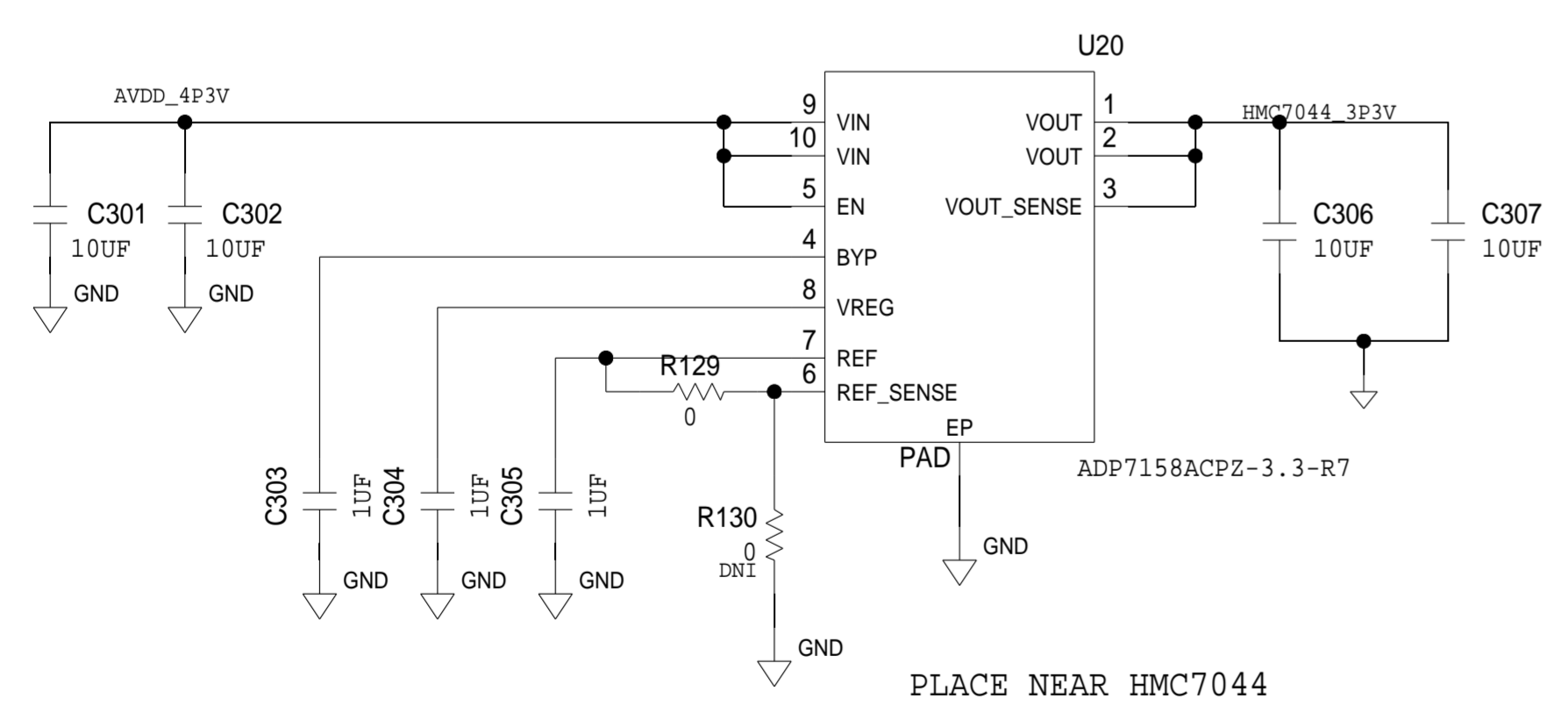
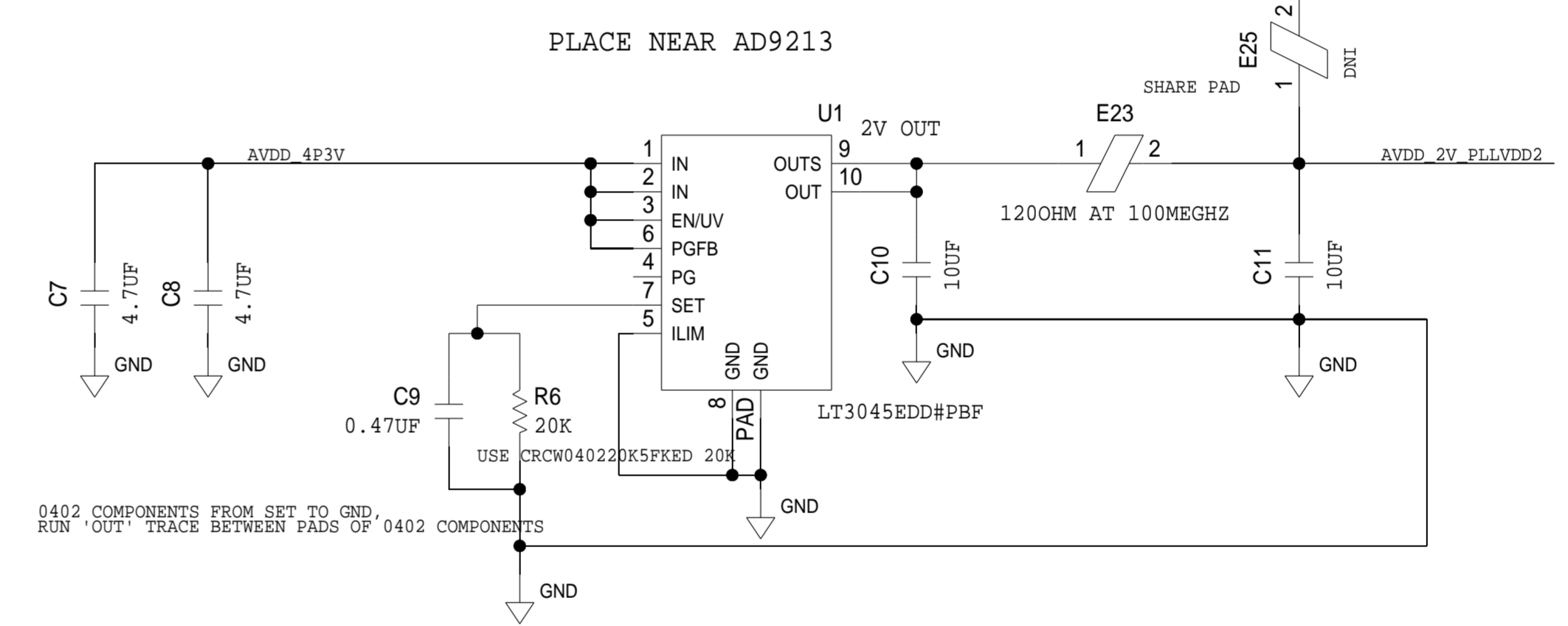
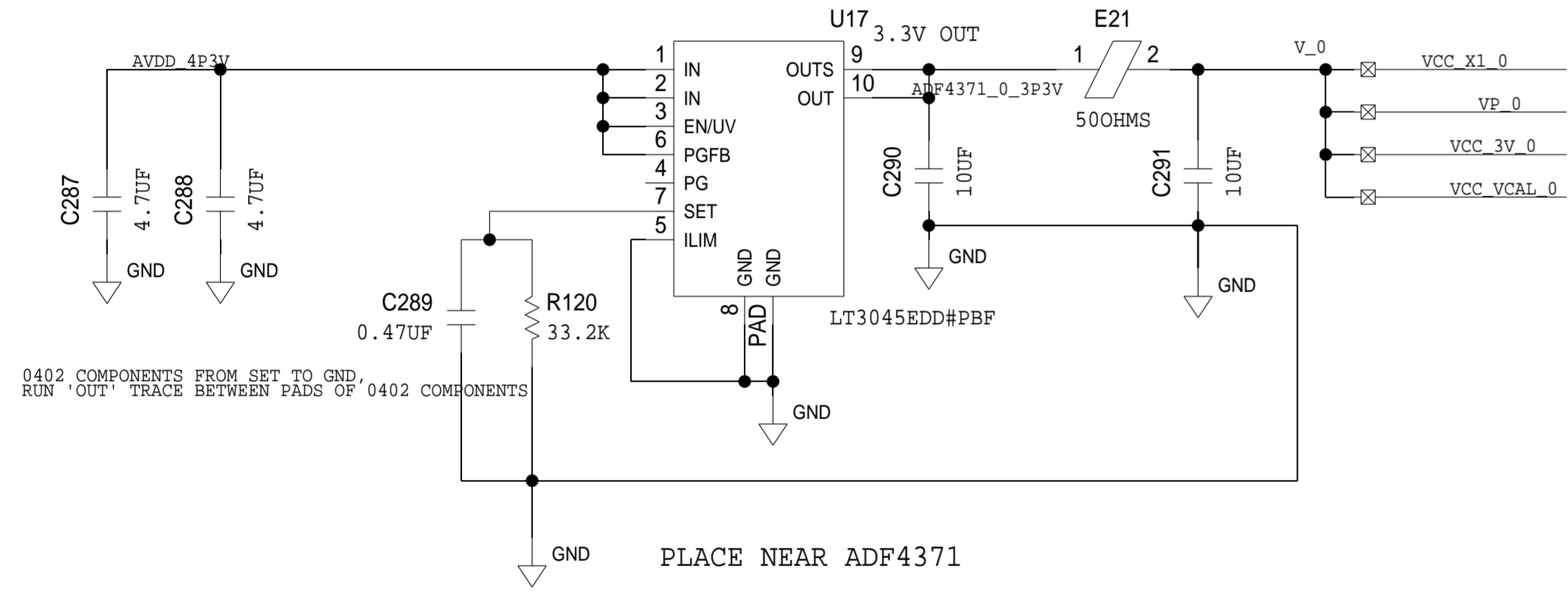
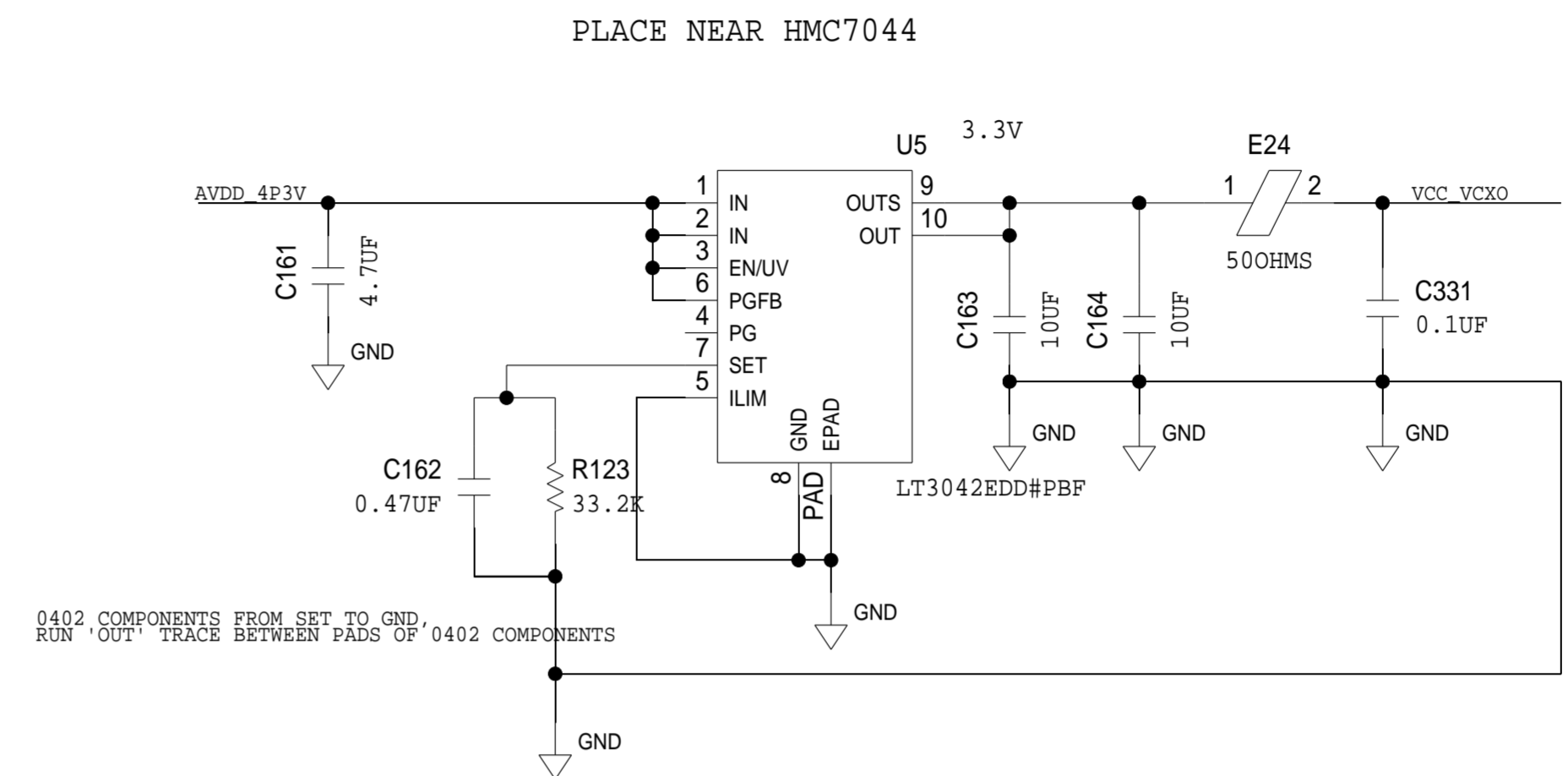
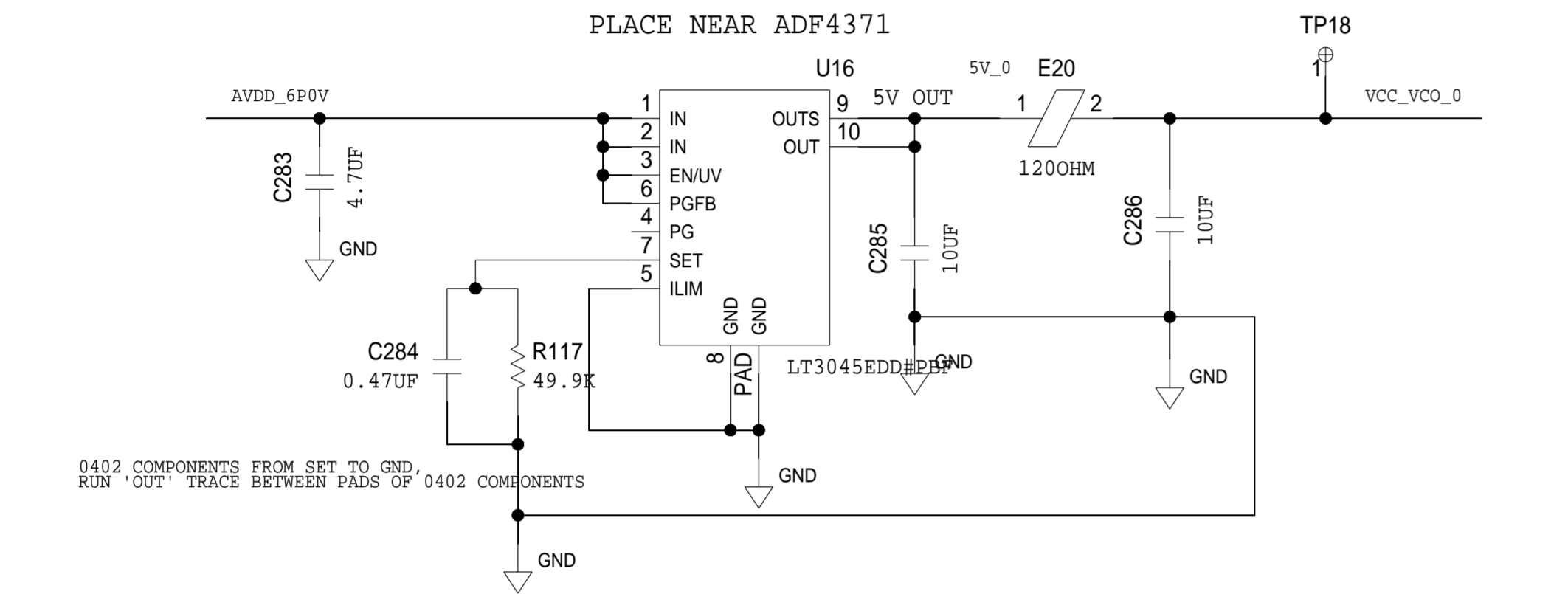
POWER FOR ADC PLL, ADF4371 AND HMC7044



SCHEMATIC				
HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		DRAWING NO. 02_064617		REV D
DESIGN VIEW <DESIGN_VIEW>		SIZE D	SCALE 1:1	SHEET 8 OF 13
PTD ENGINEER P. PASAQUIAN		THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.		

POWER FOR ADC PLL, ADF4371 AND HMC7044 (CONT.)

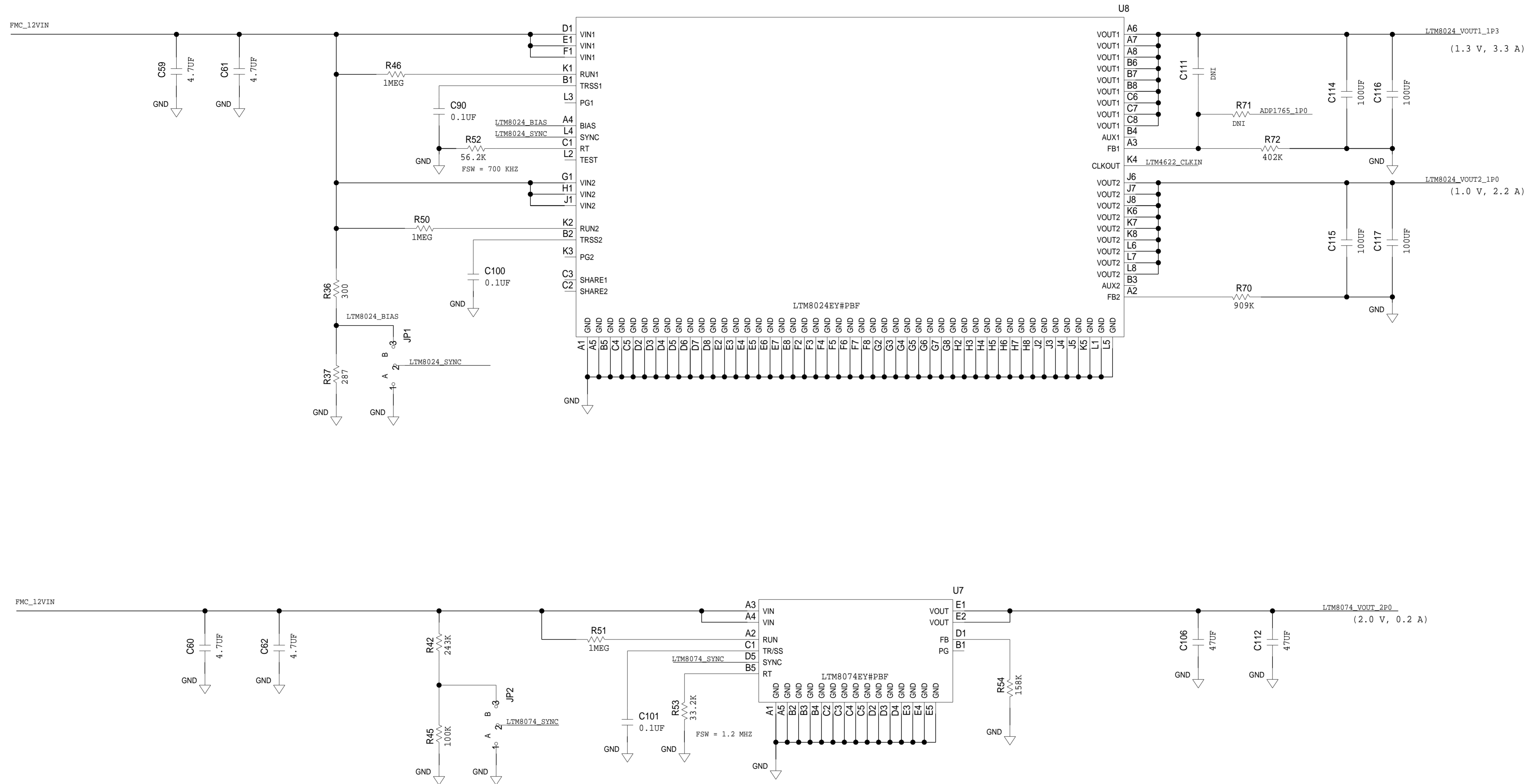
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D	
PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1	SHEET 9 OF 13

ADC POWER - SWITCHERS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

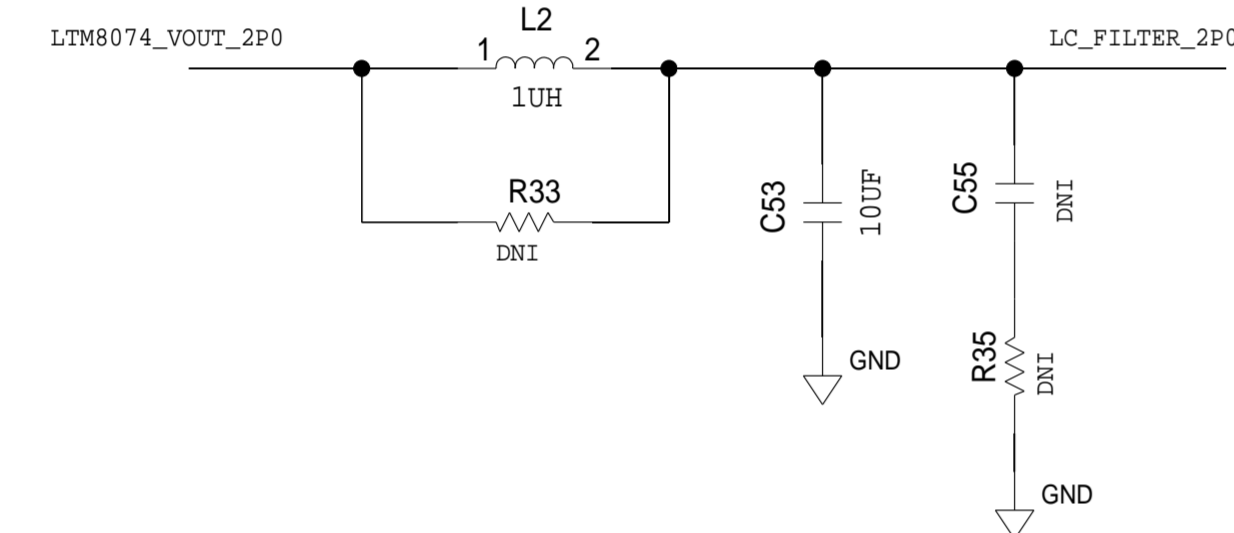
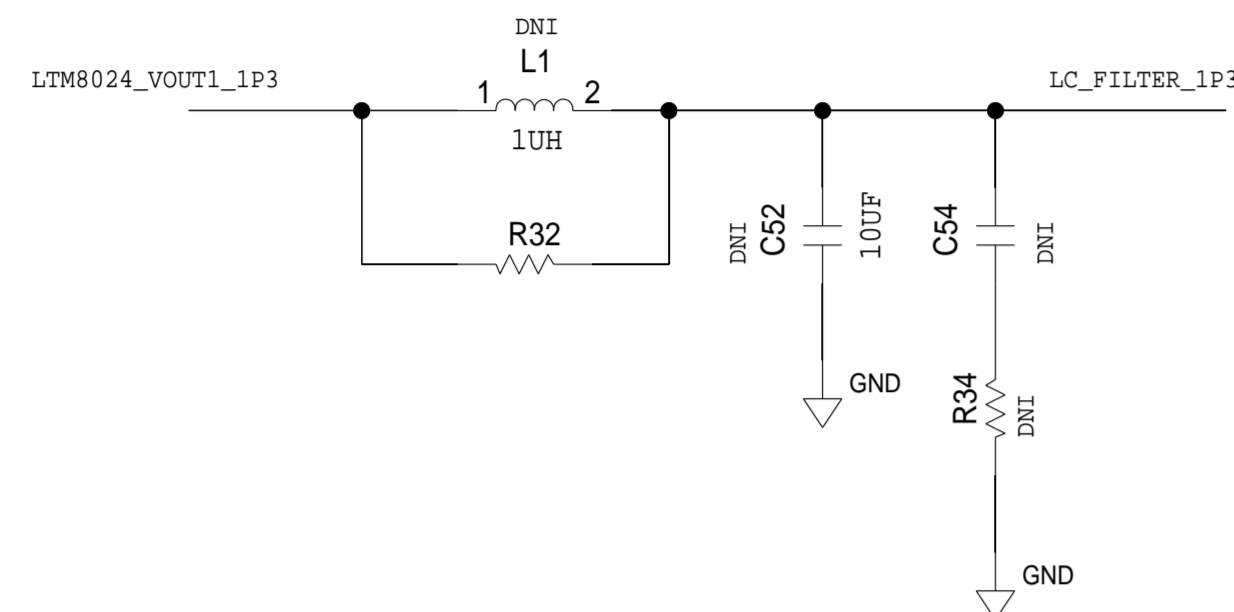


ANALOG DEVICES				SCHEMATIC			
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PTD ENGINEER P. PASAQUIAN		SIZE D		SCALE 1:1		SHEET 10 OF 13	

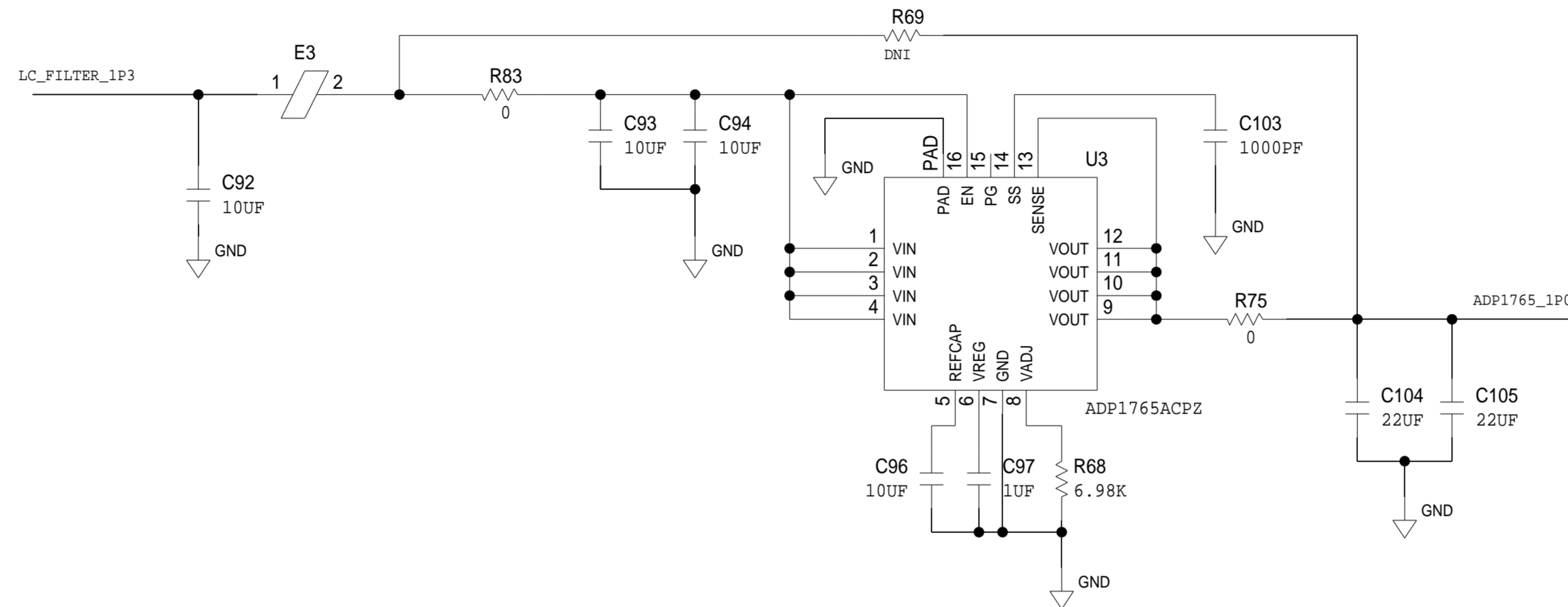
ADC POWER - POST FILTERS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

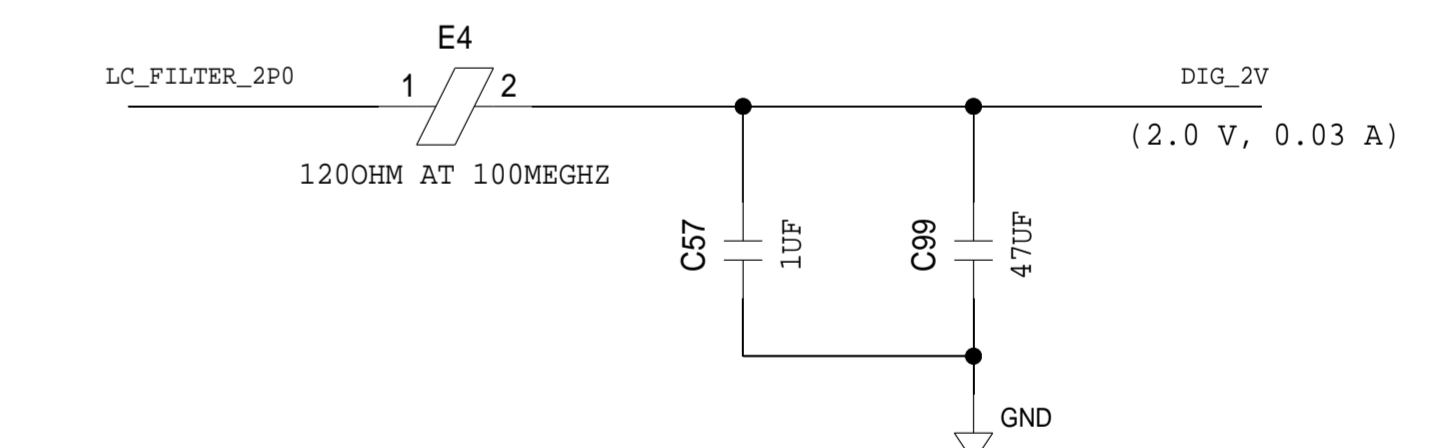
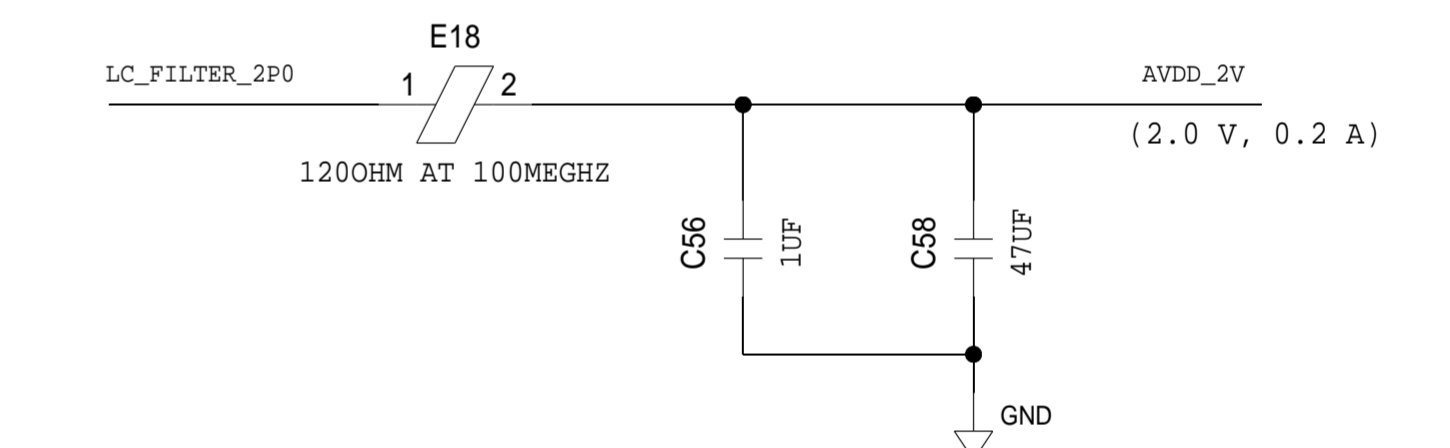
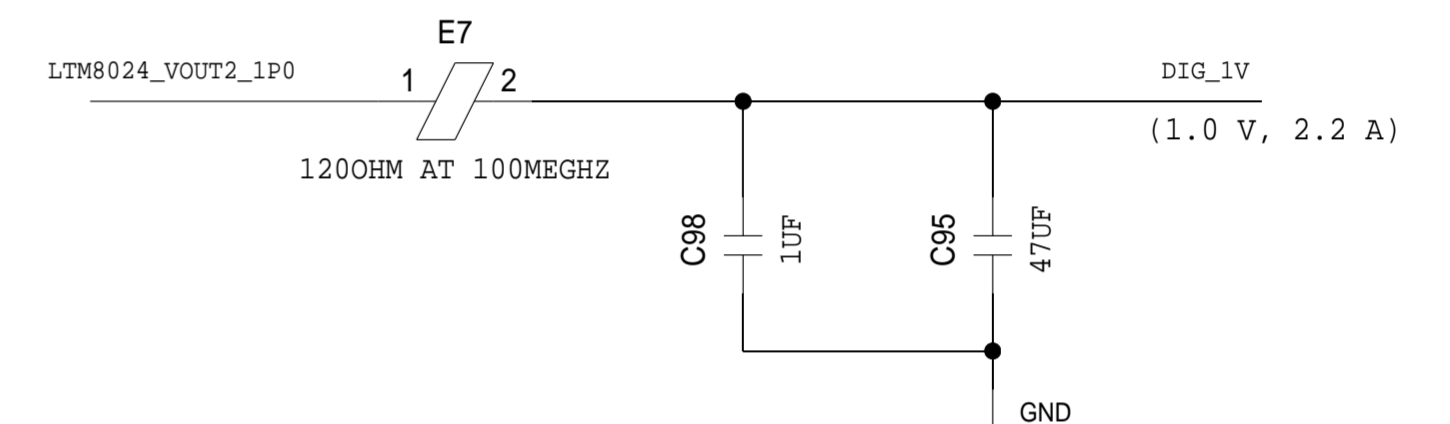
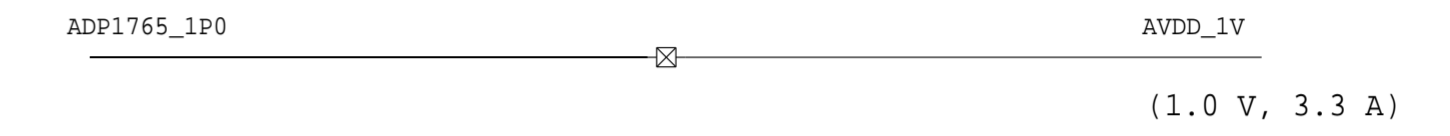
OPTIONAL LC FILTER



LDO REGULATOR



FERRITE BEAD FILTERS

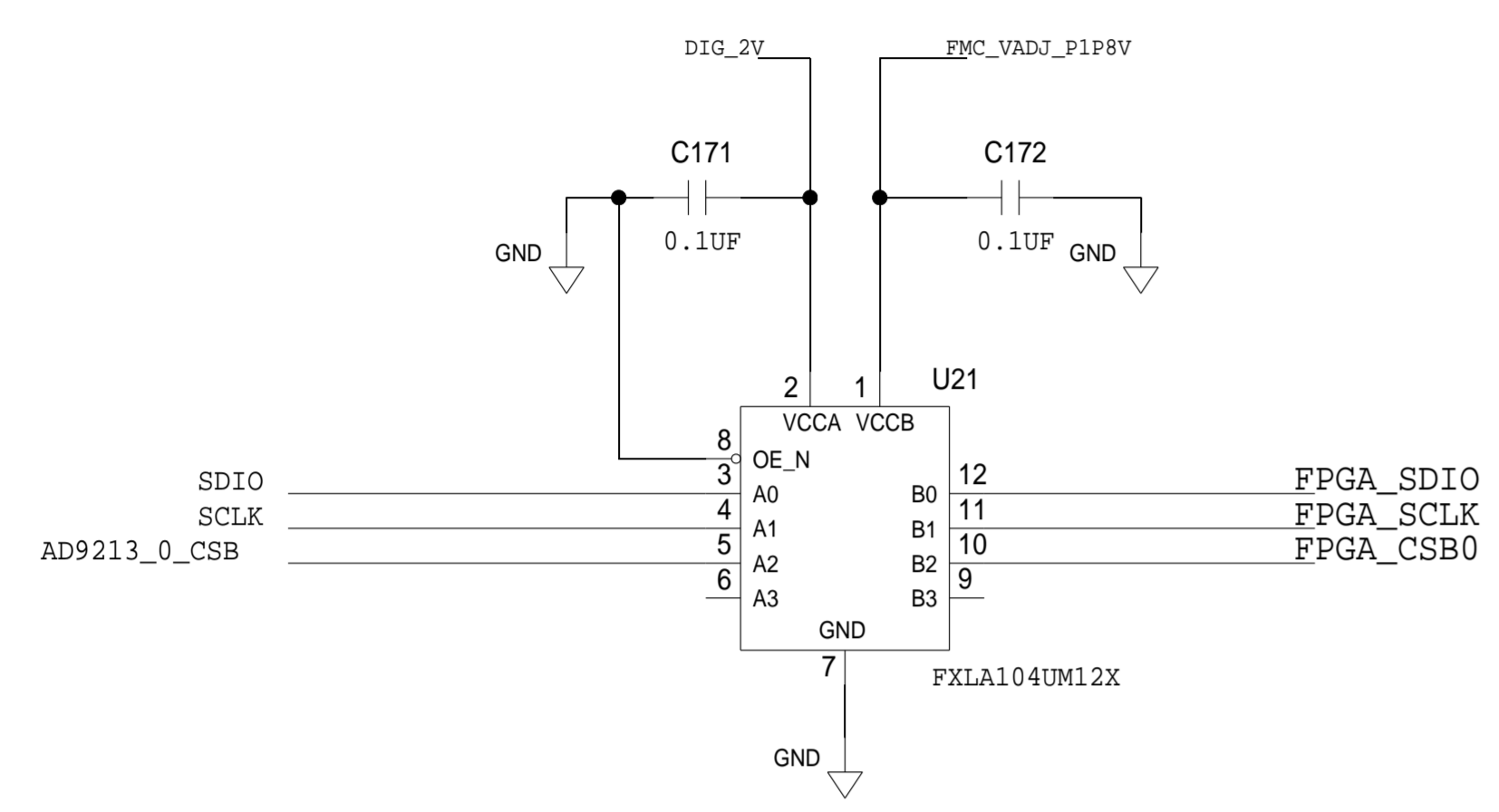


	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D
	PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1
		SHEET 11 OF 13	

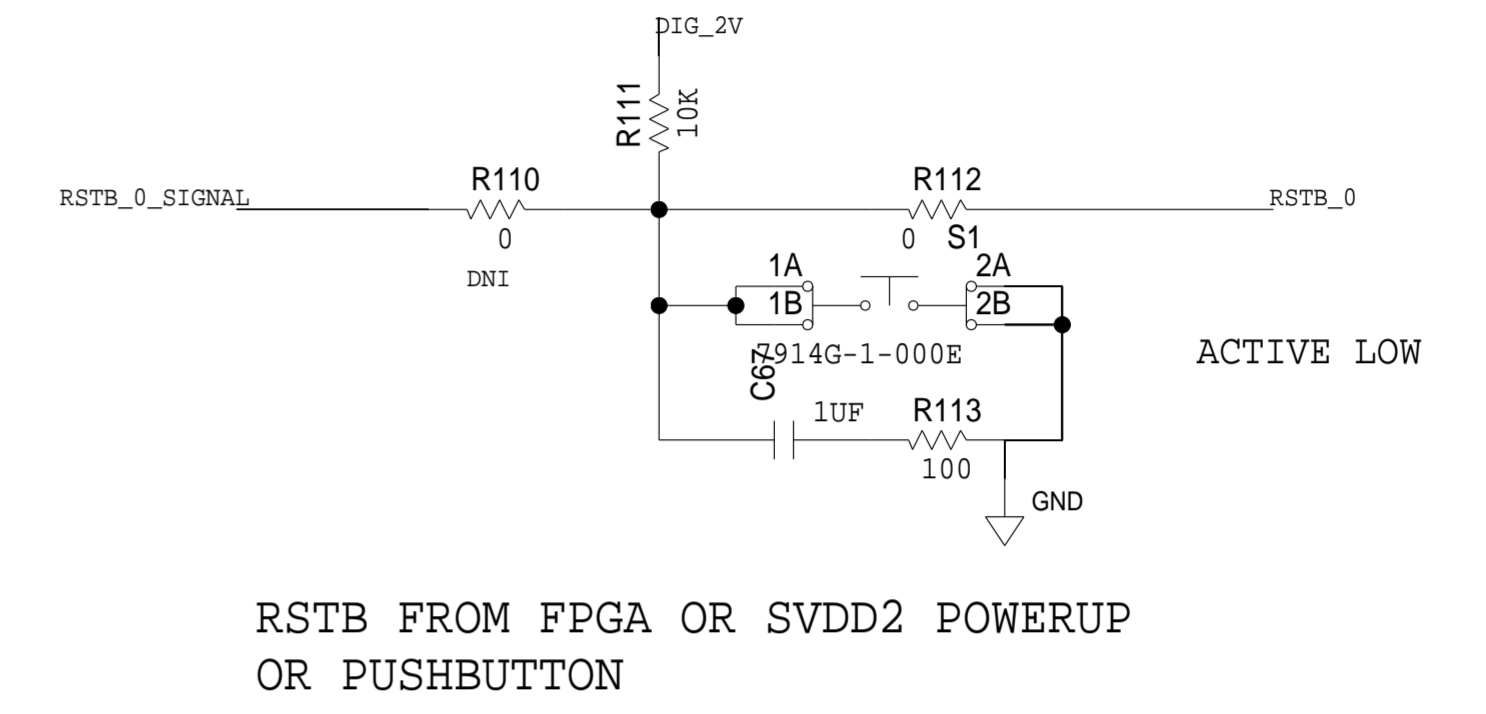
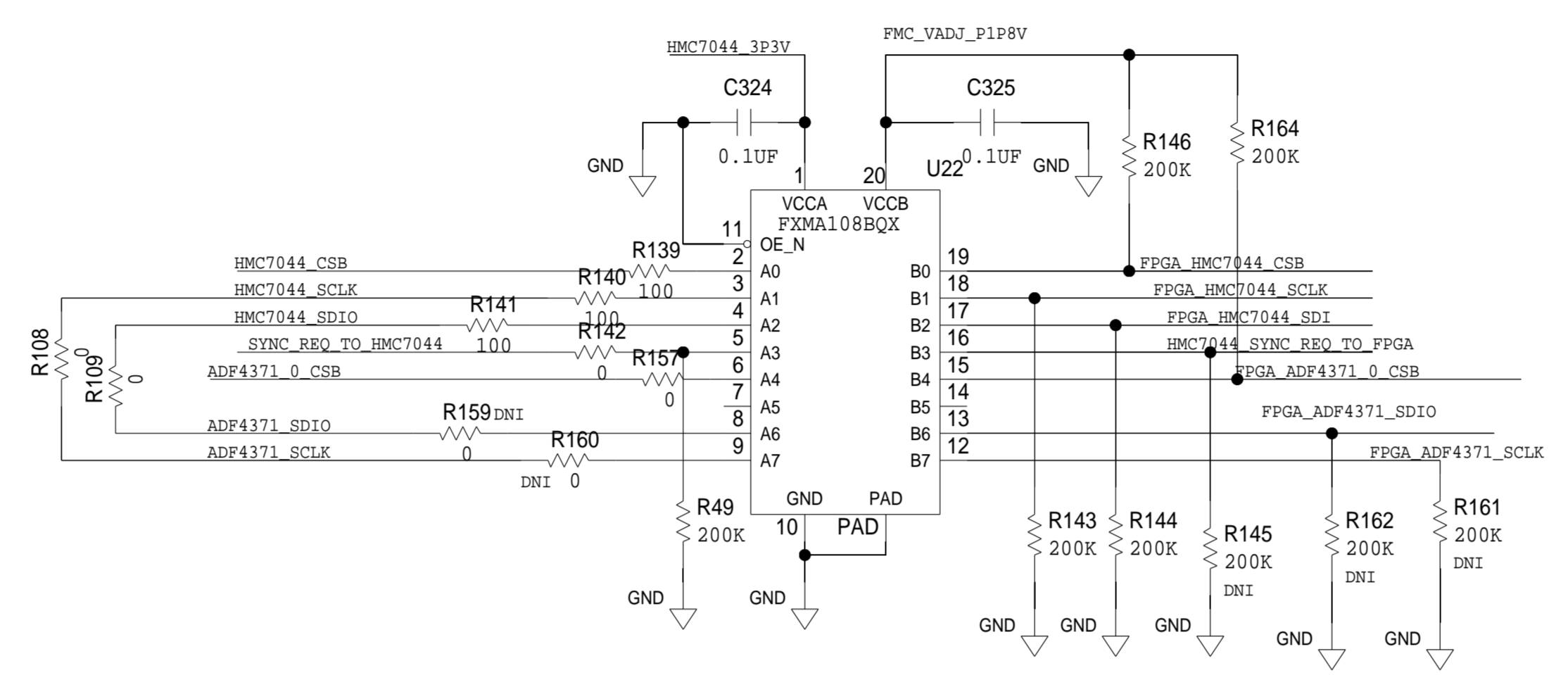
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

FMC BUFFERS - SPI, GPIO, RESET, PDWN, EEPROM, TRIG

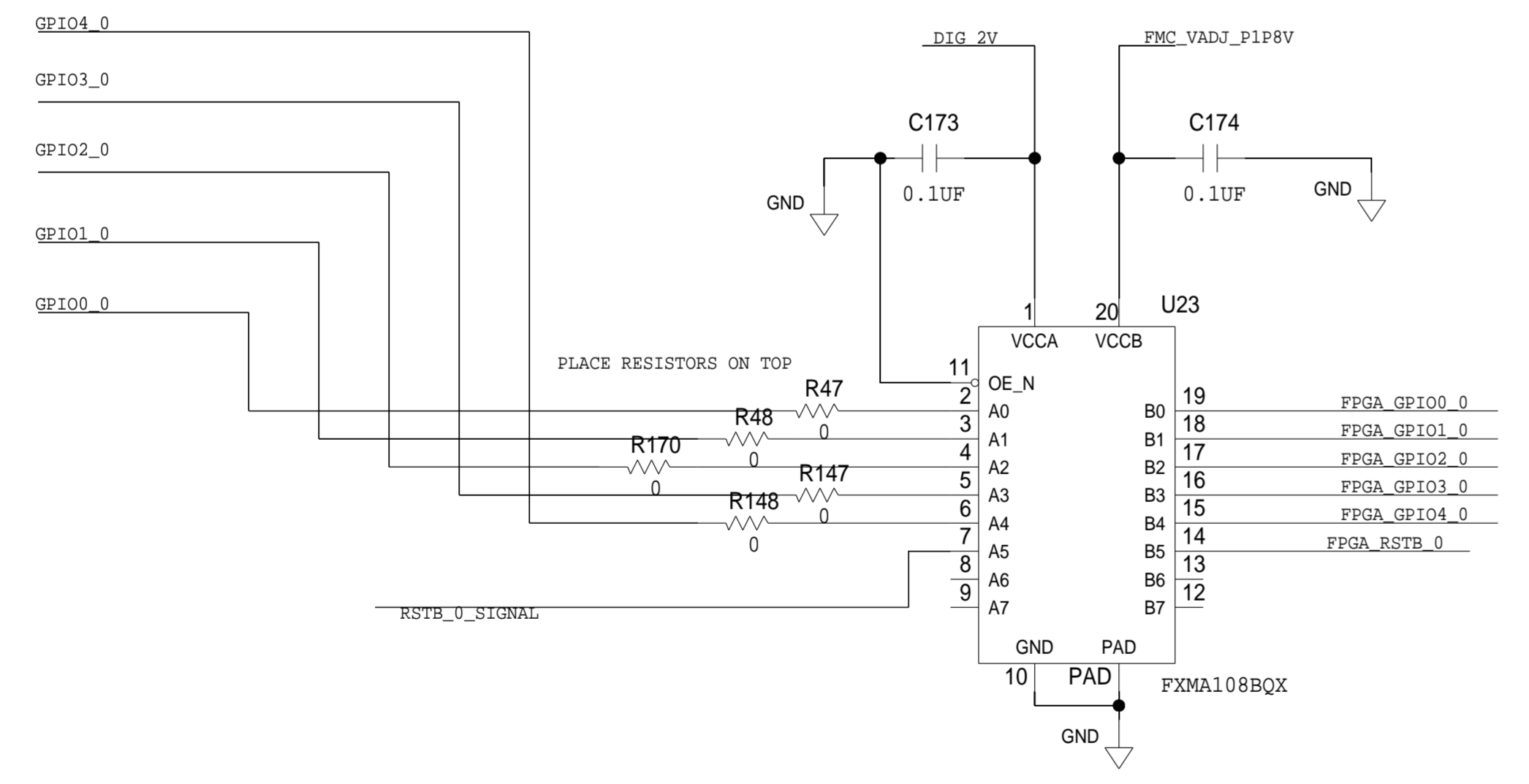
3-WIRE SPI FOR AD9213_0



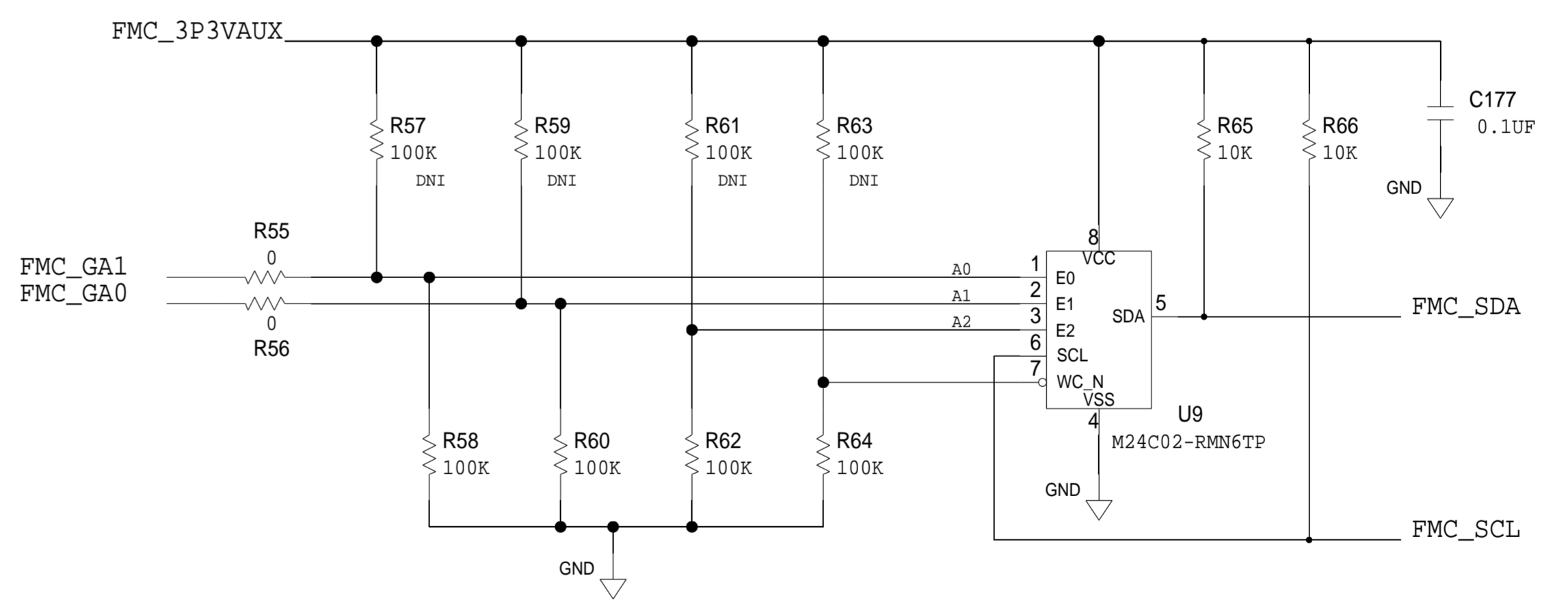
3-WIRE SPI FOR HMC7044 & ADF4371



GPIOs AND RESET



EEPROM - BOARD ID AD9213-10G-EBZ OR AD9217-10G-EBZ



	SCHEMATIC		
	HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064617	REV D
	PTD ENGINEER P. PASAQUIAN	SIZE D	SCALE 1:1
		SHEET 12 OF 13	

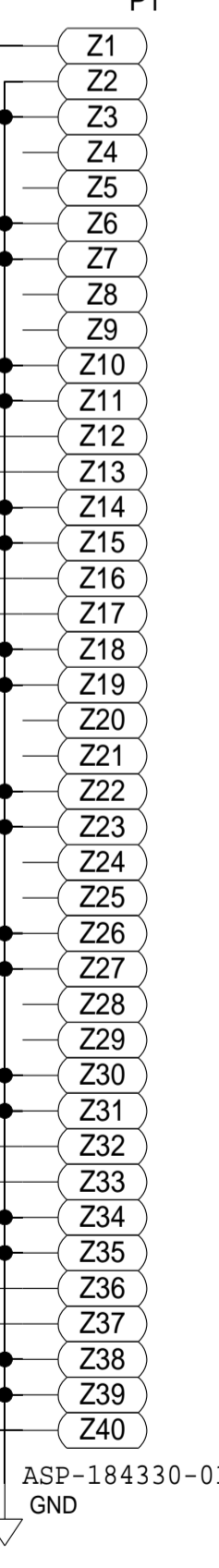
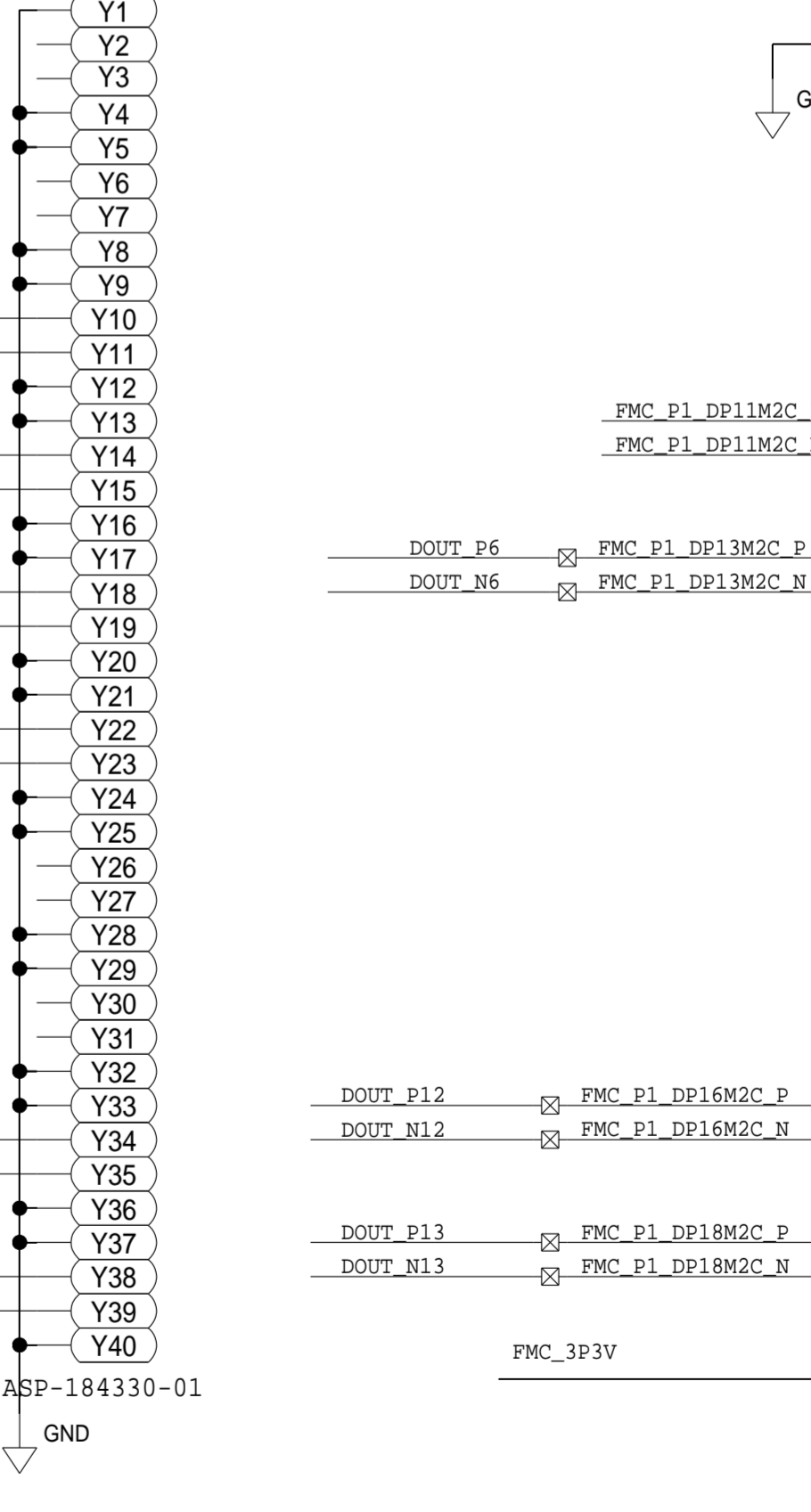
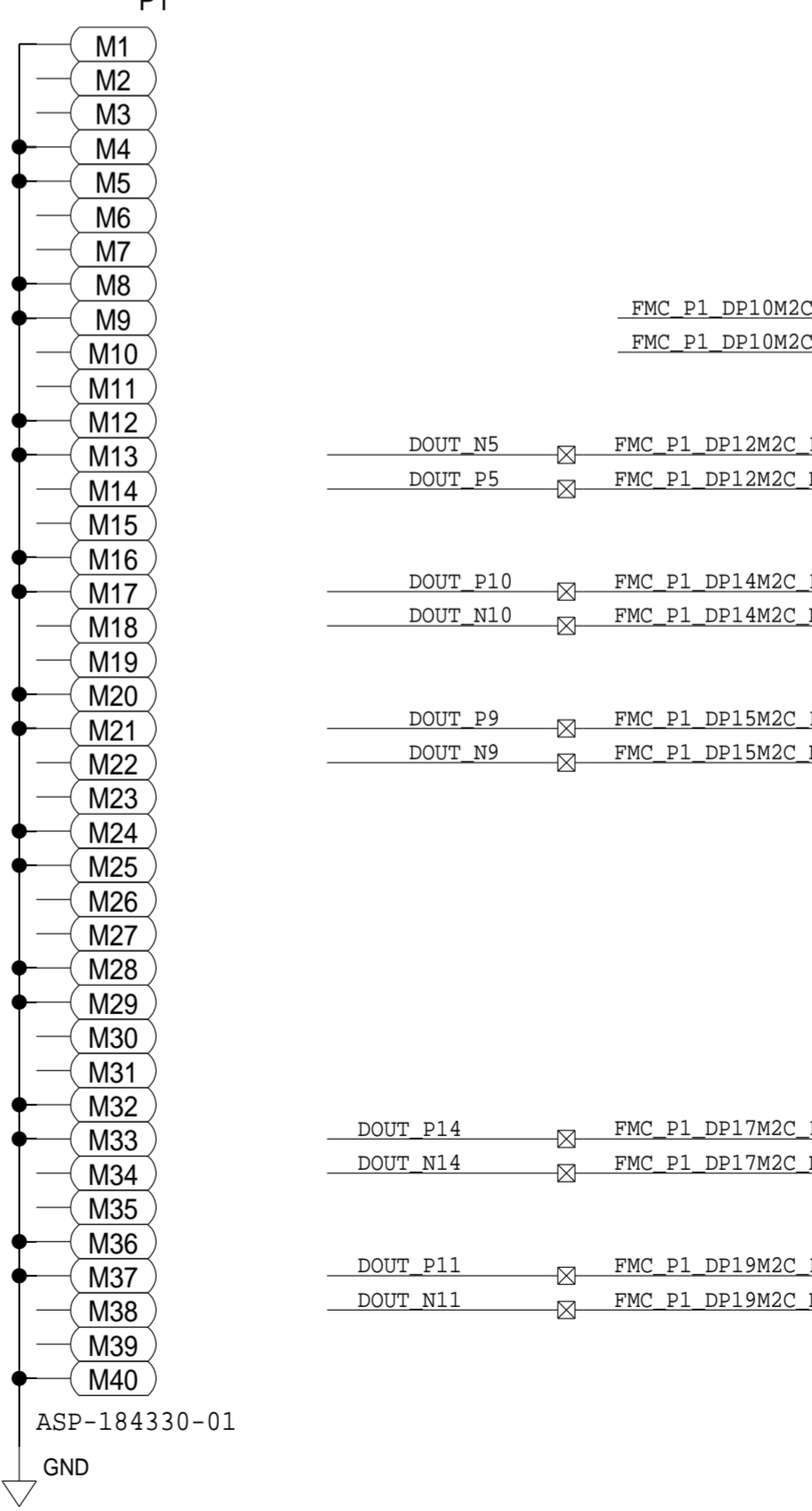
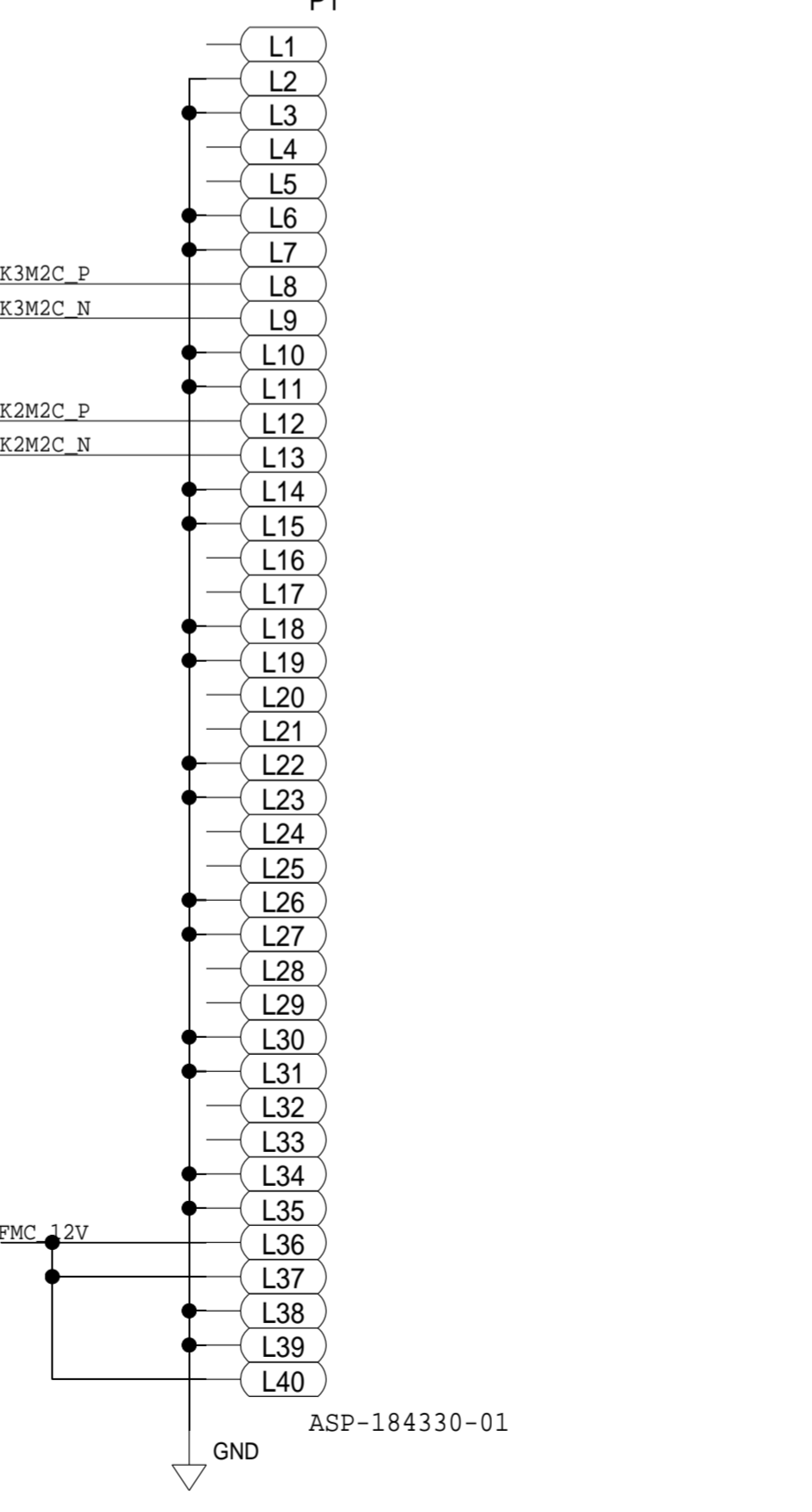
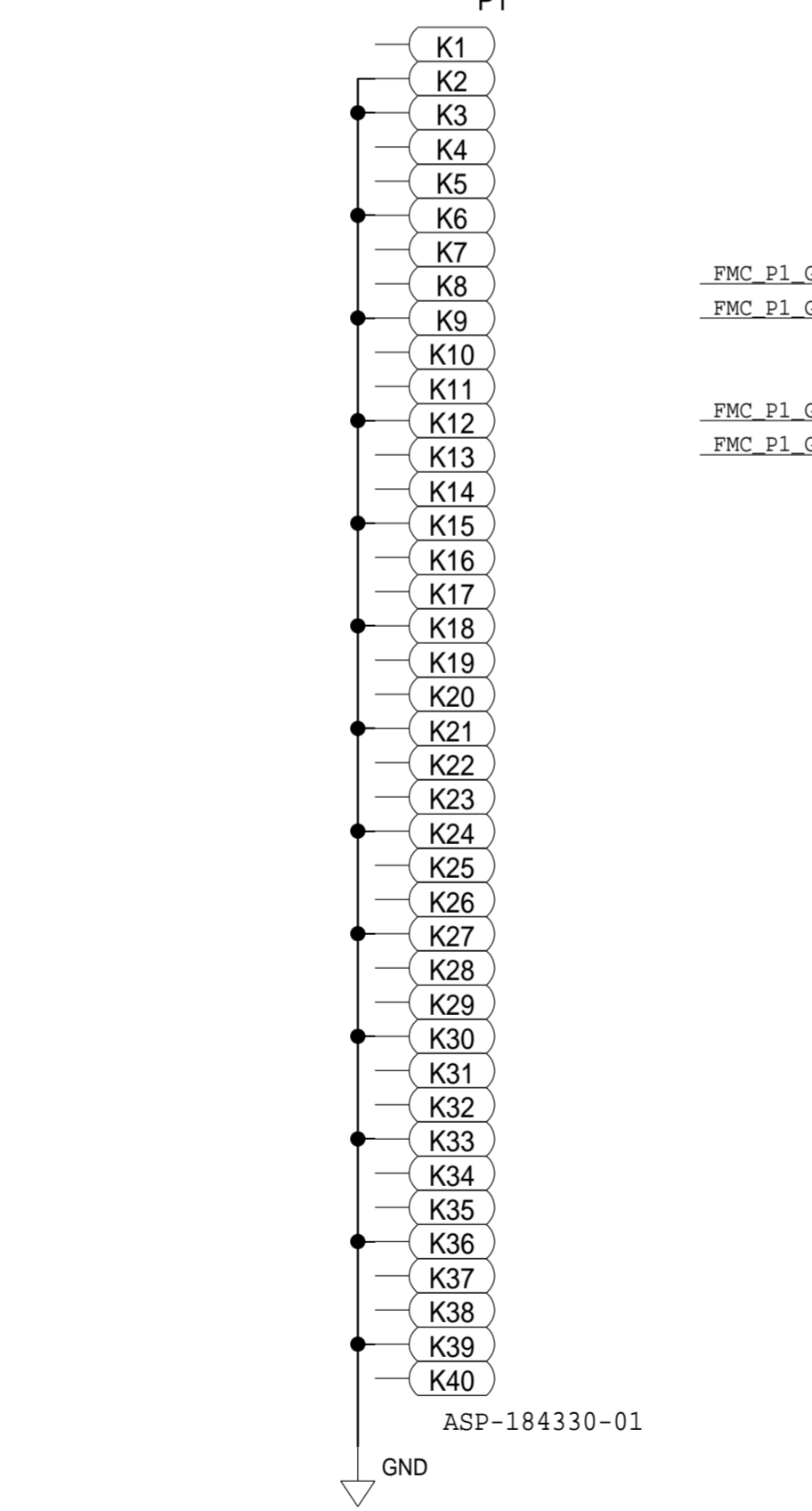
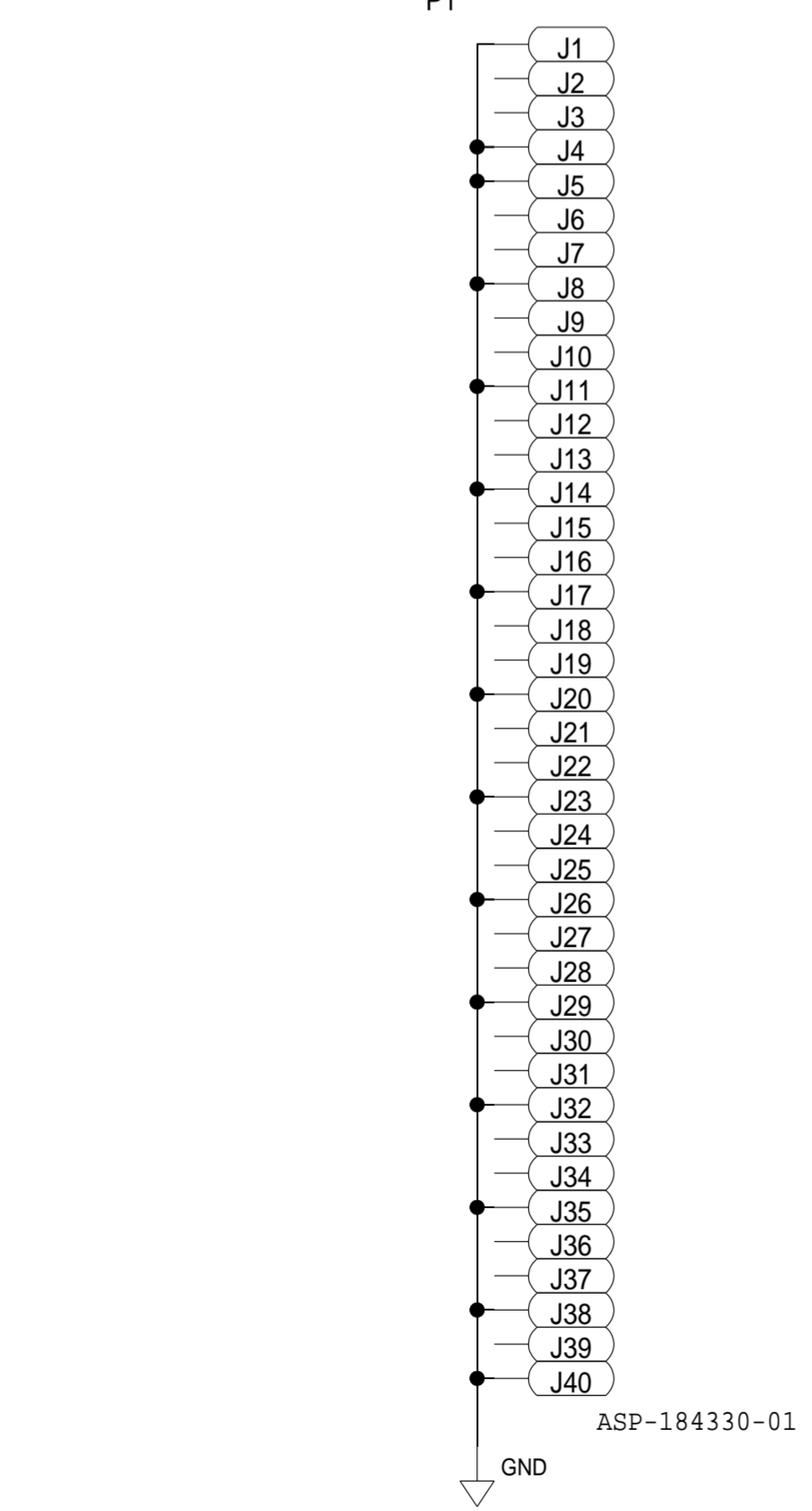
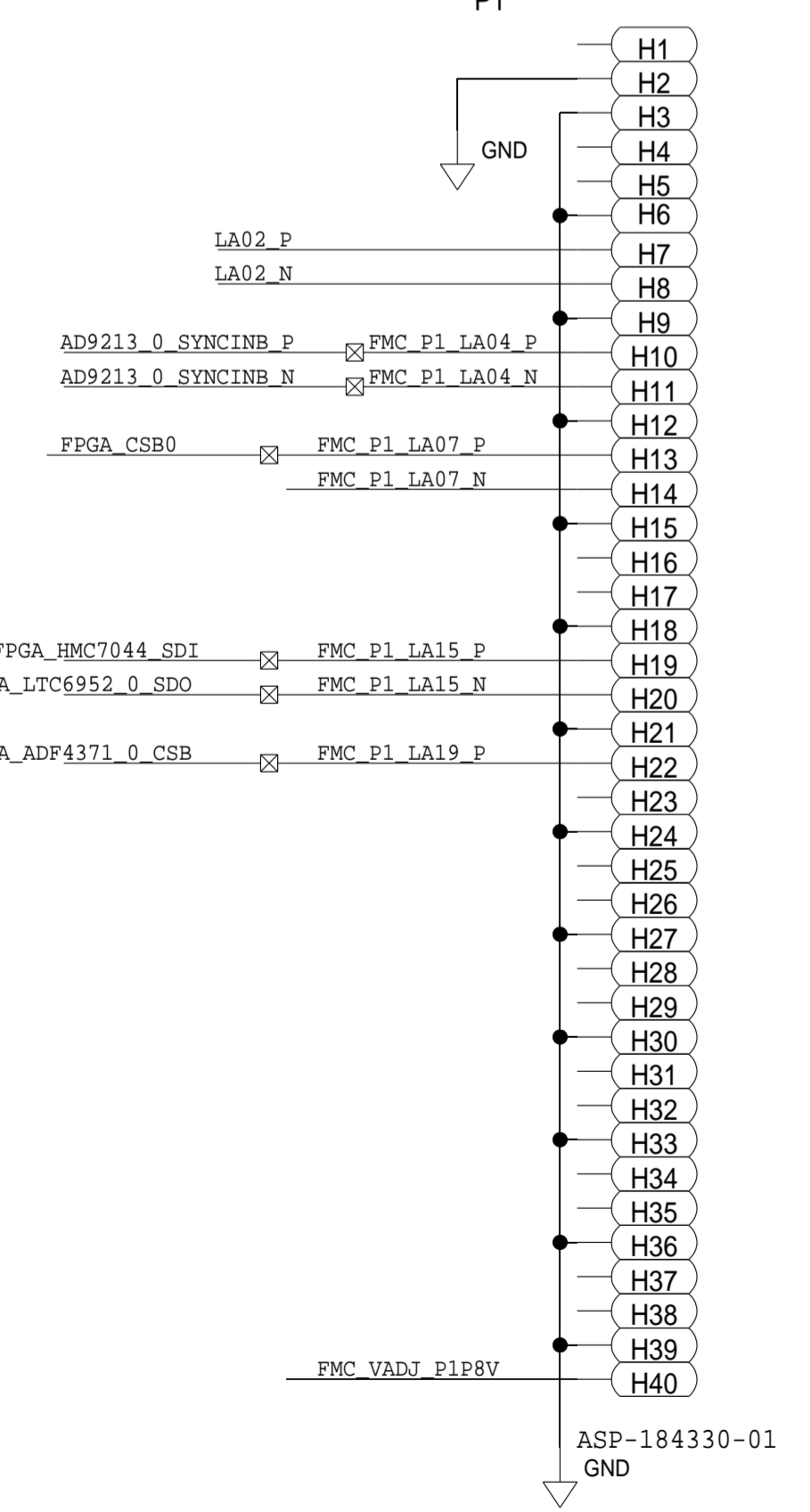
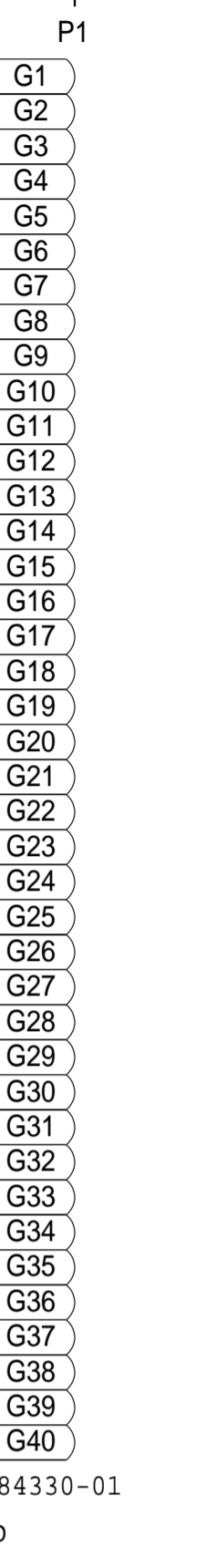
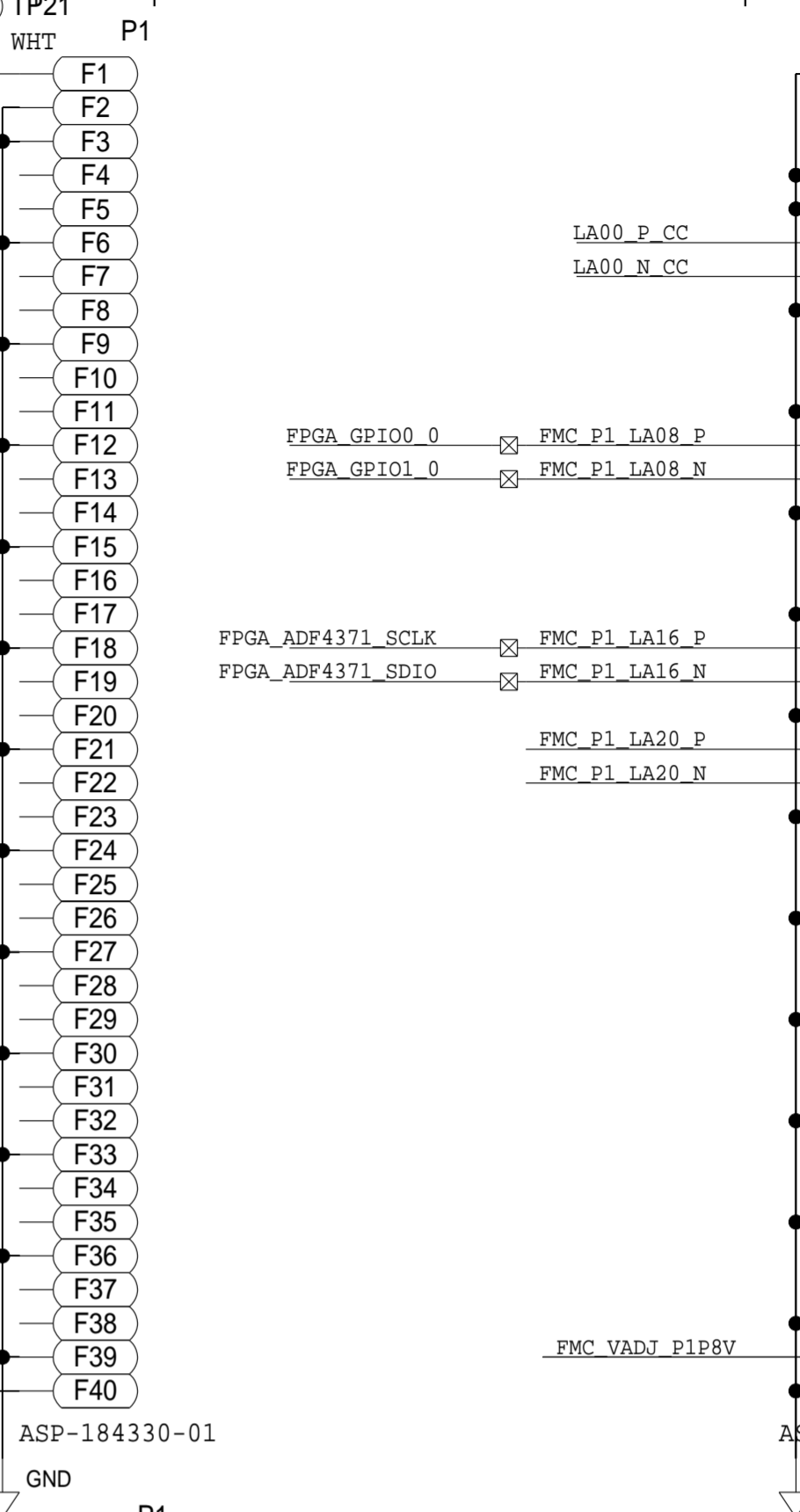
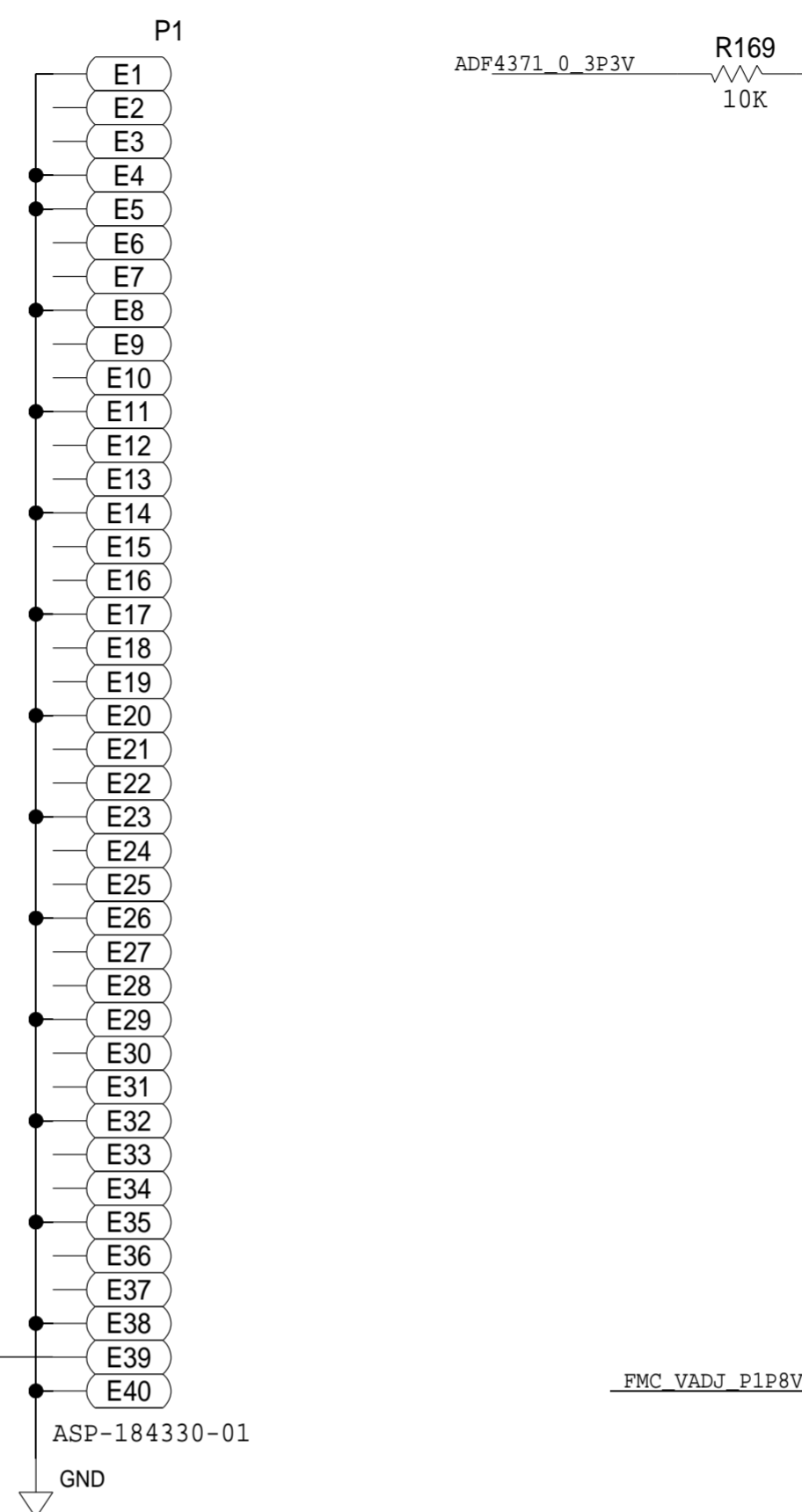
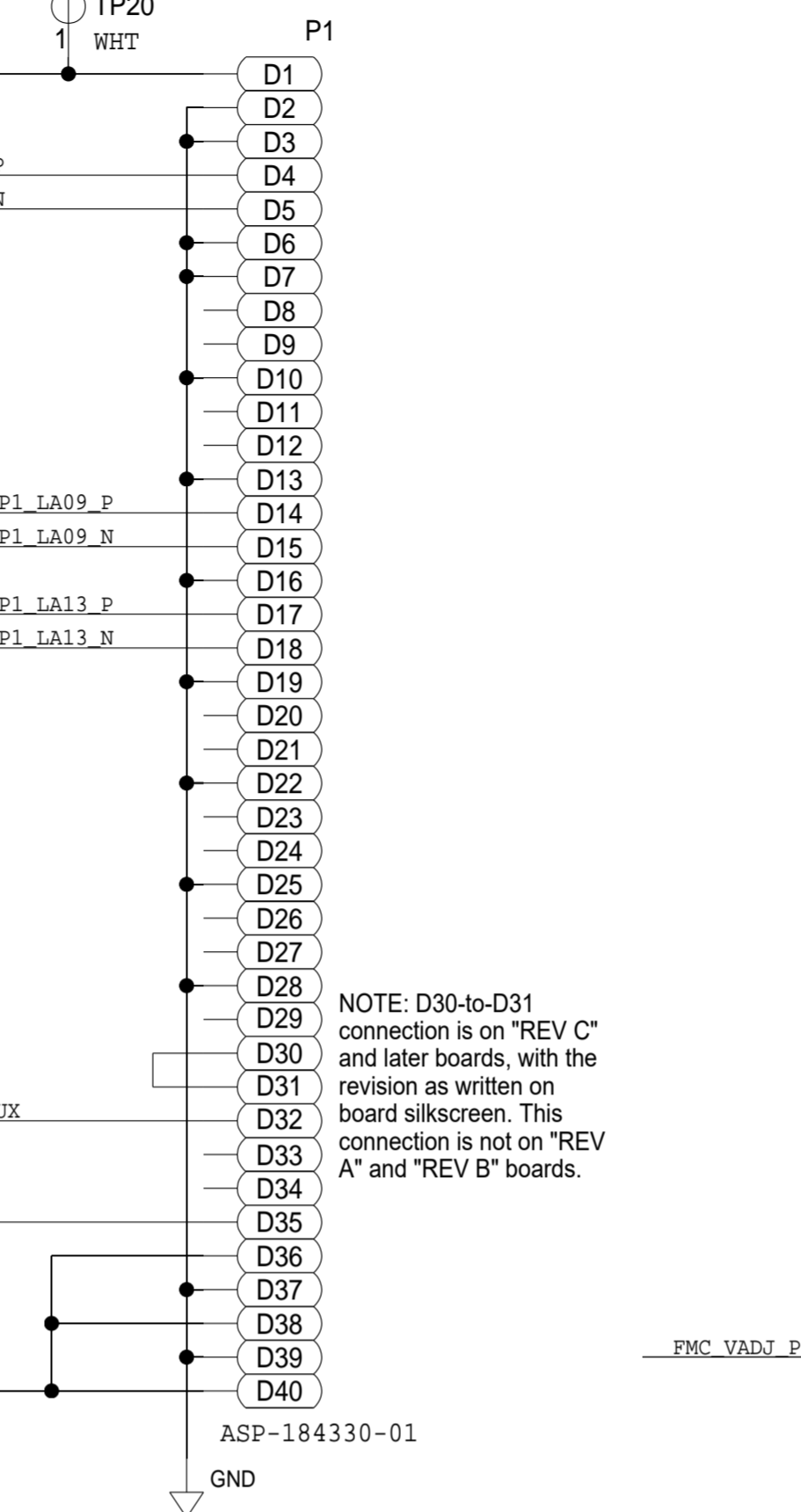
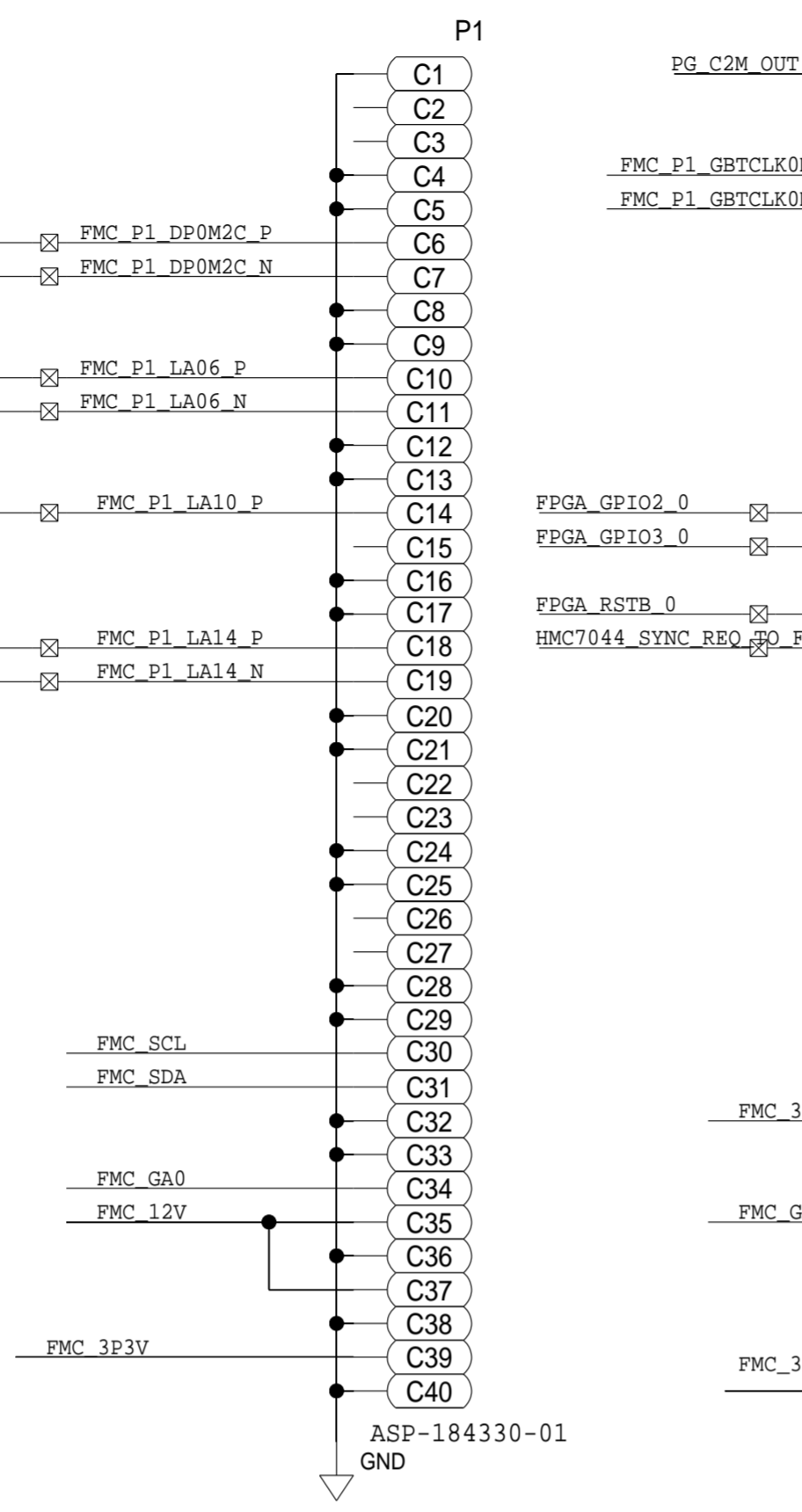
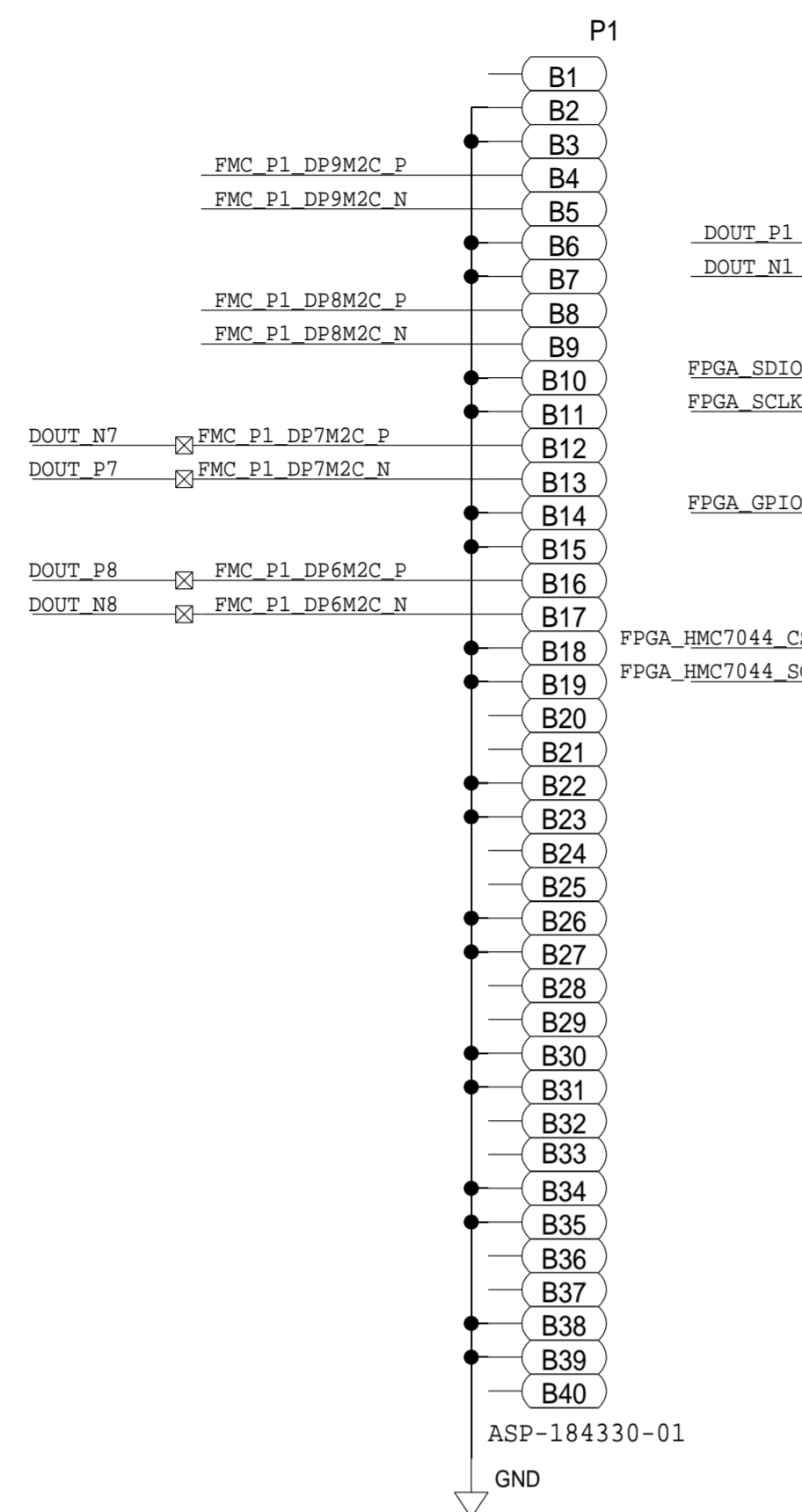
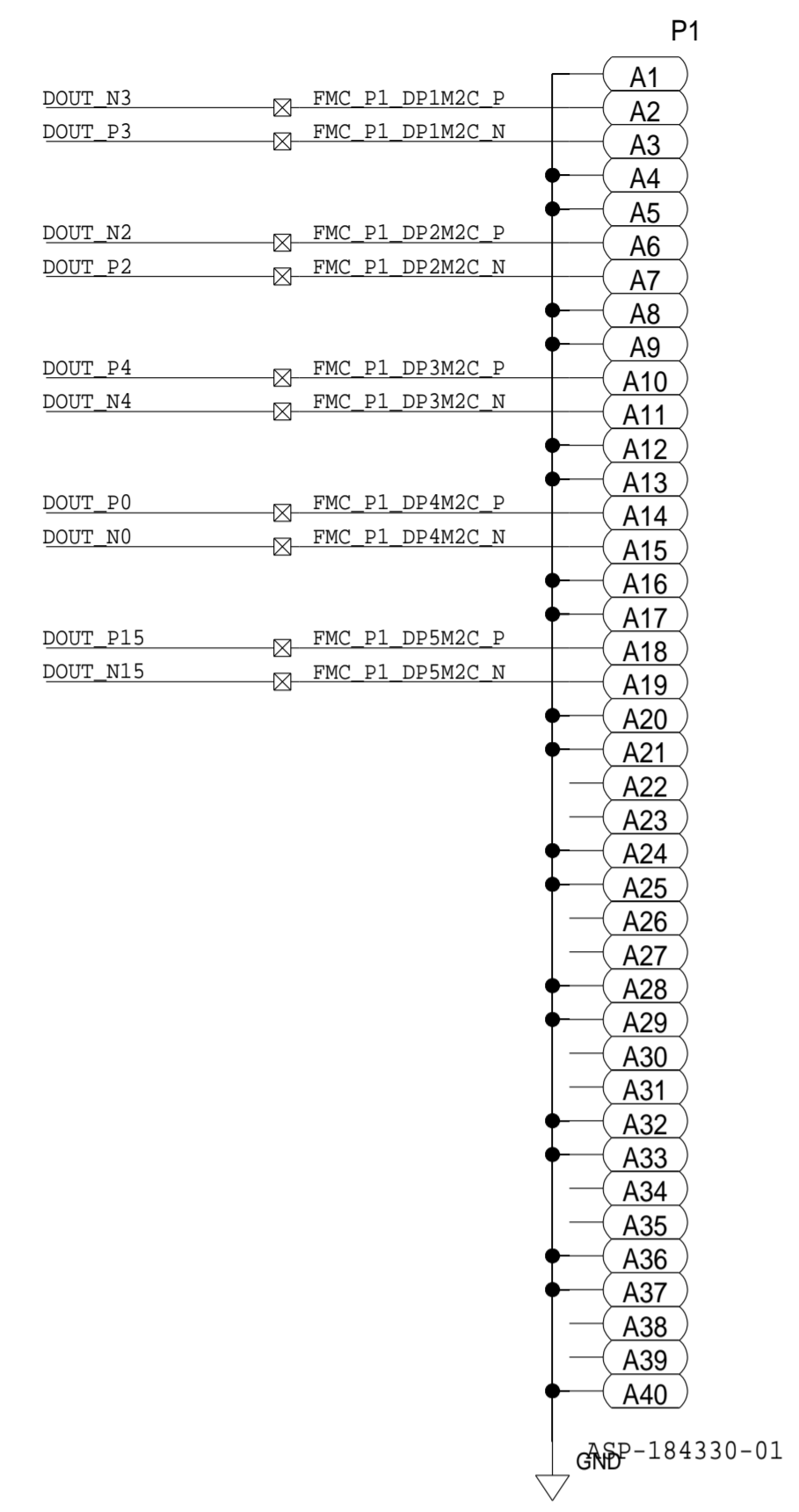
FMC+ HSPC CONNECTOR (P1)

SER_OUT PAIRS ASSIGNED TO TOP (L1) OR BOTTOM (L10)

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

D
C
B
A

D
C
B
A



NOTE: SER_OUT PINS SHOULD BE FIT INTO ADJACENT QUADS 0-3, 4-7, 8-11, 12-15, 16-19
FPGA_SYNCINB SHOULD BE DIFF CMOS DRIVE

SCHEMATIC			
		HW TYPE : Customer Evaluation Z Product(s) : ad9213 : LTM8024, LTM8078	
DESIGN VIEW	<DESIGN_VIEW>	DRAWING NO.	REV
PTD ENGINEER	P. PASAQUIAN	02_064617	D
SIZE	SCALE	SHEET 13 OF 13	
D	1:1		

8 7 6 5 4 3 2 1